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(72) Inventors:
• **Petre, Frederik**
3400 Landen (BE)
• **Leus, Geert**
3511 Hasselt (BE)

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(74) Representative: **Van Malderen, Joelle et al**
Office Van Malderen,
Place Reine Fabiola 6/1
1083 Bruxelles (BE)

(71) Applicant: **INTERUNIVERSITAIR**
MICROELEKTRONICA CENTRUM (IMEC)
3001 Leuven (BE)

(54) **CDMA transceiver techniques for multiple input multiple output (mimo) wireless communications**

(57) The present invention is related to a method for multi-user wireless transmission of data signals in a communication system having at least one base station and at least one terminal. It comprises, for a plurality of users, the following steps :

- adding robustness to frequency-selective fading to the data to be transmitted.
- performing spreading and scrambling of at least a

portion of a block of data, obtainable by grouping data symbols by demultiplexing using a serial-to-parallel operation,

- combining (summing) spread and scrambled portions of the blocks of at least two users,
- adding transmit redundancy to the combined spread and scrambled portions, and
- transmitting the combined spread and scrambled portions with transmit redundancy.

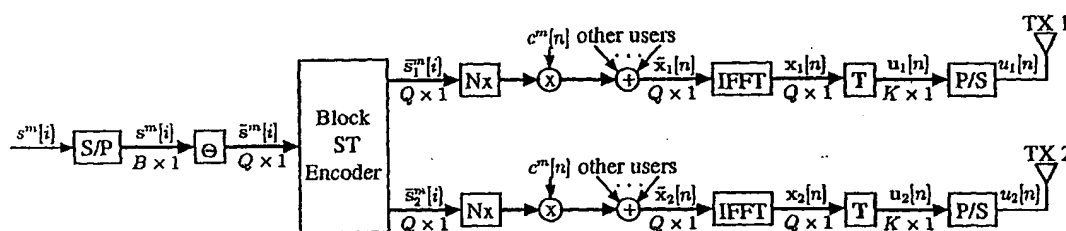


Fig. 6

Description

Field of the invention

[0001] The present invention is related to a method for Wideband Code Division Multiple Access (WCDMA) wireless communication systems, suitable for communication over frequency-selective fading channels.

Introduction to the state of the art

[0002] Wideband CDMA is emerging as the predominant wireless access mode for forthcoming 3G systems, because it offers higher data rates and supports larger number of users over mobile wireless channels compared to access techniques like TDMA and narrowband CDMA. Especially in the downlink (from base to mobile station) direction the main drivers toward future broadband cellular systems require higher data rates. There are several main challenges to successful transceiver design. First, for increasing data rates, the underlying multi-path channels become more time-dispersive, causing Inter-Symbol Interference (ISI) and Inter-Chip Interference (ICI), or equivalently frequency-selective fading. Second, due to the increasing success of future broadband services, more users will try to access the common network resources, causing Multi-User Interference (MUI). Both ISI/ICI and MUI are important performance limiting factors for future broadband cellular systems, because they determine their capabilities in dealing with high data rates and system loads, respectively. Third, cost, size and power consumption issues put severe constraints on the receiver complexity at the mobile.

[0003] Direct-Sequence (DS) Code Division Multiple Access (CDMA) has emerged as the predominant air interface technology for the 3G cellular standard, because it increases capacity and facilitates network planning in a cellular system. DS-CDMA relies on the orthogonality of the spreading codes to separate the different user signals. However, ICI destroys the orthogonality among users, giving rise to MUI. Since the MUI is essentially caused by the multi-path channel, linear chip-level equalization, combined with correlation with the desired user's spreading code, allows to suppress the MUI. However, chip equalizer receivers suppress MUI only statistically, and require multiple receive antennas to cope with the effects caused by deep channel fades.

[0004] Multiple Input Multiple Output (MIMO) systems with several transmit and several receive antennas are able to realize a capacity increase in rich scattering environments. Space-Time coding is an important class of MIMO communication techniques that achieve a high quality-of-service over frequency-flat fading channels by introducing both temporal and spatial correlation between the transmitted signals. It has already been combined with single-carrier block transmission to achieve maximum diversity gains over frequency-selective fading. Up till now however focus was mainly on single-user point-to-point communication links.

Aims of the invention

[0005] The present invention aims to provide a method and device for Wideband Code Division Multiple Access (WCDMA) wireless communication that preserves the orthogonality among users and guarantees symbol detection regardless of the frequency-selective fading channels.

Summary of the invention

[0006] The invention relates to a method for multi-user wireless transmission of data signals in a communication system having at least one base station and at least one terminal. It comprises, for a plurality of users, the following steps :

- adding robustness to frequency-selective fading to said data to be transmitted,
- performing spreading and scrambling of at least a portion of a block of data, obtainable by grouping data symbols by demultiplexing using a serial-to-parallel operation,
- combining (summing) spread and scrambled portions of said blocks of at least two users,
- adding transmit redundancy to said combined spread and scrambled portions, and
- transmitting said combined spread and scrambled portions with transmit redundancy.

[0007] Preferably the spreading and scrambling operation is performed by a code sequence, obtained by multiplying a user(terminal)-specific code and a base station specific scrambling code.

[0008] Preceding the steps mentioned above, the step can be performed of generating a plurality of independent block portions.

[0009] The method can also start with the step of generating block portions.

- [0010] Advantageously all the steps are performed as many times as there are block portions, thereby generating streams comprising a plurality of combined spread and scrambled block portions.
- [0011] In a specific embodiment, between the step of combining and the step of transmitting said spread and scrambled portions, the step is comprised of encoding each of said streams.
- 5 [0012] More specifically, the step is comprised of space-time encoding said streams, thereby combining info from at least two of said streams.
- [0013] Even more specifically the step of space-time encoding the streams is performed by block space-time encoding or trellis space-time encoding.
- 10 [0014] In an alternative embodiment, the step of inverse subband processing is comprised between the step of combining and the step of transmitting the spread and scrambled portions.
- [0015] In an advantageous embodiment the step of adding robustness to frequency-selective fading is performed by adding linear precoding.
- [0016] Alternatively, the step of adding robustness to frequency-selective fading is performed by applying adaptive loading per user.
- 15 [0017] In a typical embodiment the step of combining spread and scrambled block portions includes the summing of a pilot signal.
- [0018] In the method of the invention the step of adding transmit redundancy comprises the addition of a cyclic prefix, a zero postfix or a symbol postfix.
- [0019] The invention also relates to a transmit system device for wireless multi-user communication, applying the method here described.
- 20 [0020] Another object of the invention relates to a transmit apparatus for wireless multi-user communication, comprising :
- Circuitry for grouping data symbols to be transmitted,
 - 25 - Means for applying a spreading and scrambling operation to said grouped data symbols,
 - Circuitry for add transmit redundancy to said spread and scrambled grouped data symbols,
 - At least one transmit antenna for transmitting said spread and scrambled grouped data symbols with transmit redundancy.
- 30 [0021] In a specific embodiment the transmit apparatus also comprises means for adding robustness to frequency-selective fading to the grouped data symbols.
- [0022] In a preferred embodiment the transmit apparatus also comprises a space-time encoder.
- [0023] In another preferred embodiment the transmit apparatus also comprises circuits for inverse subband processing.
- 35 [0024] The invention also relates to a method for receiving at least one signal in a multi-user wireless communication system having at least one base station and at least one terminal, comprising the steps of
- Receiving a signal from at least one antenna,
 - Subband processing of a version of said received signal,
 - 40 - Separating the contributions of the various users in said received signal,
 - Exploiting the additional robustness to frequency-selective fading property of said received signal.
- [0025] In a particular embodiment the step of separating the contributions consists in first filtering at chip rate at least a portion of the subband processed version of said received signal and then despreading.
- 45 [0026] In another particular embodiment the step of separating the contributions consists in first despreading and then filtering at least a portion of the subband processed version of said received signal.
- [0027] In a typical embodiment the step of receiving a signal is performed for a plurality of antennas, thereby generating data streams and wherein the step of subband processing is performed on each of said data streams, yielding a subband processed version of said received signal.
- 50 [0028] In a specific embodiment the additional step of space-time decoding is performed on each of the streams.
- [0029] To be even more precise the step of space-time decoding can be performed by block decoding or trellis decoding.
- [0030] In another embodiment the additional step of inverse subband processing is performed on at least one filtered, subband processed version of the received signal.
- 55 [0031] Preferably the step of filtering is carried out by a filter of which the coefficients are determined in a semi-blind fashion or in a training-based way.
- [0032] In another embodiment the step of filtering is carried out by a filter of which the filter coefficients are determined without channel estimation.

[0033] Advantageously the step of filtering at chip rate is carried out by a filter of which the filter coefficients are determined such that one version of the filtered signal is as close as possible to a version of the pilot symbol.

[0034] More in particular, the version of the filtered signal is the filtered signal after despreading with a composite code of the base station specific scrambling code and the pilot code and wherein the version of the pilot symbol is the pilot symbol itself, put in per tone ordering.

[0035] In another particular embodiment the version of the filtered signal is the filtered signal after projecting on the orthogonal complement on the subspace spanned by the composite codes of the base station specific scrambling code and the user specific codes. The version of the pilot symbol is the pilot symbol spread with a composite code of the base station specific scrambling code and the pilot code, and put in per tone ordering.

[0036] Typically, the additional step of removing transmit redundancy is performed.

[0037] In a particular embodiment the additional robustness to fading is exploited by linear de-precoding.

[0038] The invention also relates to a receive system device for wireless multi-user communication, applying the method as described above.

[0039] Another object of the invention relates to a receiver apparatus for wireless multi-user communication, comprising :

- A plurality of antennas receiving signals,
- A plurality of circuits adapted for subband processing of said received signals,
- Circuitry being adapted for determining by despreading an estimate of subband processed symbols received by at least one user.

[0040] In an embodiment the circuitry adapted for determining an estimate of symbols comprises a plurality of circuits for inverse subband processing.

[0041] In a specific embodiment the circuitry adapted for determining an estimate of symbols further comprises a plurality of filters to filter at least a portion of a subband processed version of said received signals.

[0042] Even more specifically the filtering is performed at chip rate.

[0043] Finally, the apparatus further comprises a space-time decoder.

Short description of the drawings

[0044]

Fig. 1 represents a telecommunication system in a single-cell configuration.

Fig. 2 represents a telecommunication system in a multiple-cell configuration.

Fig. 3 represents a block diagram of a receiver structure.

Fig. 4 represents the Multi-Carrier Block-Spread CDMA downlink transmission scheme.

Fig. 5 represents the MUI-resilient MCBS-CDMA downlink reception scheme.

Fig. 6 represents the Space-Time Block Coded MCBS-CDMA downlink transmission scheme.

Fig. 7 represents the MUI-resilient STBC/MCBS-CDMA MIMO reception scheme.

Fig. 8 represents the transmitter model for Space-Time Coded MC-DS-CDMA with Linear Precoding.

Fig. 9 represents the receiver model for Space-Time Coded MC-DS-CDMA with Linear Precoding.

Fig. 10 represents the transmitter model for Space-Time Coded SCBT-DS-CDMA.

Fig. 11 represents the receiver model for Space-Time Coded SCBT-DS-CDMA.

Fig. 12 represents the base station transmitter model for SCBT-DS-CDMA with KSP.

Fig. 13 represents the mobile station receiver model for SCBT-DS-CDMA with KSP.

Detailed description of the invention

[0045] In the invention methods for W-CDMA wireless communication between devices and the related devices are presented (Fig 1). In the communication system at least data (10) is transmitted from at least one base station (100) to at least one terminal (200). The communication method is extendable to a case with a plurality of base stations (Fig 2, 100, 101), each base station being designed for covering a single cell (Fig 2, 20, 21) around such base station. In such multiple base station and hence multicell case a terminal receives typically signals from both the most nearby base station and other base stations. Within the method it is assumed that the base station has at least one antenna (110) and the terminal also has at least one physical antenna (210). The communication between the base station(s) and the terminal is designed such that said communication is operable in a context with multiple terminals. Hence it is assumed that substantially simultaneous communication between said base station(s) and a plurality of terminals is taking place, while still being able to distinguish at the terminal side which information was intended to be transmitted

to a dedicated terminal.

[0046] The notion of a user is introduced. It is assumed that with each terminal in such a multi-terminal context at least one user is associated. The invented communication method and related devices exploit spreading with orthogonal codes as method for separating information streams being associated with different users. Hence at the base station side information, more in particular data symbols, of different users, hence denoted user specific data symbols are available. After spreading spread user specific data symbols are obtained. These spread user specific data symbols are added, leading to a sum signal of spread user specific data symbols. Further additional scrambling of said sum signal is performed by a scrambling code being base station specific. Symbols obtained after spreading or spreading and scrambling, and summing are denoted chip symbols. In the invented communication method blocks with a plurality of said chip symbols are transmitted. In a single base station case the transmitted signal thus comprises a plurality of time overlapped coded signals, each coded signal being associated to an individual user and distinguishable only by a user specific encoding, based on the user signature or spreading codes. In a multiple base station context, the distinguishing also exploits said base station specific code. Further such blocks have at least one pilot symbol (also called training sequence), being predetermined and known at both sides of the transmission link.

[0047] In many embodiments of the invented method the availability of a receiver being capable of generating at least two independent signals from a received signal is foreseen. Said receiver receives a spread-spectrum signal, corresponding to a superposition of the signals of all users active in the communication system or link, more in particular said superposition of signals is channel distorted. Said generation of at least two independent signals can be obtained by having at least two antennas at the terminal, each independent signal being the signal received at such antenna after the typical down-converting and filtering steps.

[0048] Recall that the invention exploits spreading with orthogonal codes for separating different users. Unfortunately said channel distortion is destroying the orthogonality of the used codes, leading to a bad separation. This problem is known as multi-user interference (MUI). Hence it is a requirement for the method of the invention to allow retrieval of a desired user's symbol sequence from a received signal transmitted in a communication context with severe multi-user interference. An additional aid in achieving this goal can come from the use of transmit redundancy. Applying transmit redundancy helps to remove or at least to weaken the effect of the time dispersion of the multi-path channel. A well known example of this is the addition of a cyclic prefix in a multi-carrier system. The method of the invention comprises a step of inputting or receiving said received signal, being a channel distorted version of a transmitted signal comprising a plurality of user data symbol sequences, each being encoded with a known, user specific code.

[0049] The multi-channel propagation also gives rise to multipath fading, which generally exhibits both frequency-selectivity and time-selectivity. The phenomenon can give rise to serious performance degradation and constitutes a bottleneck for higher data rates. Frequency-selective fading can be tackled in several ways, as is discussed below.

[0050] In the method of the invention the multi-user interference is suppressed by performing operations on the chip symbols. This multi-user interference suppression is obtained by combining said independent signals resulting in a combined filtered signal. In an embodiment of the invention said combining, also denoted chip-level equalization, is a linear space-time combining. For said combining a combiner filter (chip-level equalizer) is used. The (chip-level equalization) filter coefficients of said combiner filter are determined directly from said independent signals, hence without estimating the channel characteristics. One can state that from said independent signals in a direct and deterministic way a chip-level equalization filter is determined. Said chip-level equalization filter is such that said transmitted signal is retrieved when applying said filter to said received signal.

[0051] In the approach of the invention all system parameters are chosen such that the orthogonality between the various users is maintained, i.e. the MUI is combated in the most efficient way. To obtain that goal orthogonal user specific spreading codes are used, a block spreading operation is applied to the symbols and transmit redundancy is added such that the time dispersion is absorbed sufficiently. In this way the various users can properly be decoupled. A block spreading operation is realized by converting a symbol sequence into blocks by a serial-to-parallel conversion. In order to enhance each user's robustness against deep fading effects one additionally applies techniques like linear precoding or adaptive loading.

[0052] In case channel state information is available at the transmitter, e.g., for stationary or low-speed users, multicarrier transmission allows to apply adaptive loading to exploit the inherent frequency diversity of the channel without adding extra redundancy. Since the different users are perfectly decoupled, adaptive loading can be performed on a per user basis, such that for every user the optimal transmit spectrum can be obtained without having to bother about the presence of other users. In specific, adaptive loading assigns more information (through higher order constellations) and more power to the good subcarriers (with a high channel gain) while less information (through lower order constellations) and less power, or even none at all, is assigned to the bad subcarriers (with a low channel gain).

[0053] In case no channel state information is available at the transmitter, e.g., for medium- to highspeed users, linear precoding can be applied to robustify the transmission against frequency-selective fading. Specifically, at the transmitter, the information symbols are linearly precoded on a subset of subcarriers, while adding some well-defined redundancy, such that the information symbols can always be uniquely recovered from this subset, even if some of

the subcarriers are in a deep channel fade. At the receiver, the available frequency diversity is then exploited by performing either joint or separate equalization and decoding.

[0054] Figure 3 shows a general scheme of a receiver system. The elements presented there are used in various combinations in the embodiments described below. One or preferably several antennas (210) are foreseen for receiving signals. Next circuits (400) are provided to apply a subband processing to said received signals. The subband processed receive signals are then applied to a block (500) adapted for determining an estimate of the data symbols of at least one user. Hereby use can be made of several other functional blocks, performing the tasks of inverse subband processing (510), removing robustness added to the transmitted symbols in order to provide protection against frequency-selective fading (520), filtering (530) and/or despreading (540). Subband processing of a data signal having a data rate, comprises in principle of splitting said data signal in a plurality of data signals, with a lower data rate and modulating each of said plurality of data signals with another carrier. Said carriers are preferably orthogonal. In an embodiment said subband processing of a data signal can be realized by using serial-to-parallel convertors and using a transformation on a group of data samples of said data signal. Which operations are effectively applied and in which order highly depends on the specific embodiment. More details are given below.

[0055] In a first embodiment of the invention a multi-carrier block-spread CDMA transceiver is disclosed that preserves the orthogonality between users and guarantees symbol detection. In this approach the M user data symbol sequences are transformed into a multi-user chip sequence. Apart from the multiplexing and the inverse subband processing three major operations are performed : linear precoding, block spreading and adding transmit redundancy. Each user's data symbol sequence is converted into blocks and spread with a user specific composite code sequence being the multiplication of an orthogonal spreading code specific to the user and a base station specific scrambling code. The chip block sequences of other users are added and the resulting sum is IFFT transformed to the time domain. Then transmit redundancy is added to the chip blocks to cope with the time-dispersive effect of multi-path propagation. The sequence that comes out is then transmitted. At the receiver side perfect synchronization is assumed. In the mobile station of interest the operations corresponding to those at the transmitter side are performed. The added transmit redundancy is removed and a FFT operation is performed. The FFT output is despread with the desired user's composite code sequence. This operation decouples the various users in the system, i.e. all MUI is successfully eliminated. For each individual user an equalization filter is then provided. The equalizer filters can be designed for jointly equalizing and decoding or for separately performing said operations.

[0056] In a second embodiment of the invention space-time coding techniques, originally proposed for point-to-point communication links, are extended to point-to-multipoint communication links. The multiple access technique in the design of the transmission scheme is thereby taken into account. Each user's data symbol sequence is converted into blocks. The resulting block sequence is then linearly precoded as to add robustness against frequency-selective fading. The precoded data are put on the various tones of a multi-carrier system. Next the blocks are demultiplexed into a number of parallel sequences. Each of the sequences is spread with the same user code sequence, being the multiplication of the user specific orthogonal spreading code and the base-station specific scrambling code. In each of the parallel streams the different user chip block sequences are added up together with the pilot chip block sequence. Then a block space-time encoding operation takes place. The space-time coding is implemented on each tone separately at the base station. In stead of a block space-time encoding also a trellis space-time encoding scheme may be envisaged. The usual multicarrier operations of inverse fast Fourier transforming and adding a cyclic prefix then follow in each stream before transmitting the signal. The cyclic prefix represents in this case said transmit redundancy. The mobile station of interest at the receiver side is equipped with multiple receive antennas. The operations corresponding to those at the transmitter side are performed on each of the received signals, starting with the cyclic prefix removal and the FFT operation. The space-time block decoding operation is performed. The space-time decoded output is next re-ordered on a tone-per-tone base. Next per-tone chip equalization is applied. The filter coefficients can be determined in a training based or in a semi-blind way. In the training-based approach one relies on the knowledge of a pilot symbol. The equalizer coefficients are determined such that the equalized output after despreading is as close as possible to a version of the pilot symbol, being the pilot symbol itself, put in per tone ordering. In the semi-blind approach one relies not only on the knowledge of said pilot symbol, but also on characteristics of the codes. The equalizer filter output is projected on the orthogonal complement on the subspace spanned by the composite codes of the various users (i.e. the codes resulting from the multiplication of the base station specific scrambling code and the user specific codes). This projected output must then be as close as possible to the pilot symbol spread with a composite code of the base station specific scrambling code and the pilot code, and put in per tone ordering. From the equalizer output the contribution of that specific user can easily be derived after removing the additional robustness against deep fading.

[0057] In a third embodiment the base station again has multiple transmit antennas, but data is transmitted on a single carrier. The data symbol sequence is demultiplexed into several streams, which subsequently are converted into symbol blocks that are spread with the same user composite code sequence being the multiplication of the user specific orthogonal spreading code and the base station specific scrambling code. The pilot chip block sequence is added to the different user chip block sequences. The chip blocks output by said encoder are padded with a zero

postfix, parallel-to-serial converted and sent to the transmit antenna. Said postfix provides transmit redundancy. The receiver is again equipped with multiple receive antennas. Suppose the mobile station of interest has acquired perfect synchronization. After conversion into chip blocks the data on each receive antenna are block space-time decoded. Each decoded block is transformed into the frequency domain. The per receive antenna ordering is transformed into a per tone ordering. Then again a per tone chip level equalization can be performed. After re-transforming to the time domain and removal of the zero postfix, despreading finally yields an estimation of the desired user's transmitted data symbols.

[0058] In a fourth embodiment the base station has a single transmit antenna, whereas the mobile station of interest may have multiple receive antennas. Each user's data symbol sequence is converted into symbol blocks and spread with the user composite code sequence, being the multiplication of a user specific and a base station specific scrambling code. The pilot symbol is treated in the same way. The different user chip block sequences and the pilot chip block sequence are added. At the end of each block a number of zeros are padded. Also a known symbol postfix, the length of which is the same as the number of zeros padded, is added to each block. After P/S conversion the chip sequence is transmitted. At the receiver side the mobile station of interest is equipped with multiple antennas and has acquired perfect synchronisation. Assuming the known symbol postfix is long enough, there is no interblock interference present in the received signal. After transformation into the frequency domain a per tone chip equalizer filter is foreseen for each antenna. By transforming back into the time domain and removing the known symbol postfix the desired user's symbols can be retrieved.

[0059] Below various embodiments of the invention are described. The invention is not limited to these embodiments but only by the scope of the claims.

A. Embodiment

A.1 Transceiver design

[0060] Given the asymmetric nature of broadband services requiring much higher data rates in downlink than in uplink direction, we focus on the downlink bottleneck of future broadband cellular systems. Our goal is to design a transceiver that can cope with the three main challenges of broadband cellular downlink communications. First, multi-path propagation gives rise to time dispersion and frequency-selective fading causing ISI and ICI, which limit the maximum data rate of a system without equalization. Second, multiple users trying to access common network resources may interfere with each other, resulting in MUI, which upperbounds the maximum user capacity in a cellular system. Specific to DS-CDMA downlink transmission, the MUI is essentially caused by multi-path propagation, since it destroys the orthogonality of the user signals. Third, cost, size and power consumption issues put severe constraints on the receiver complexity at the mobile.

[0061] Throughout the text, we consider a single cell of a cellular system with a Base Station (BS) serving M active Mobile Stations (MSs) within its coverage area. For now, we limit ourselves to the single-antenna case and defer the multi-antenna case to later.

A.1.a Multi-carrier block-spread CDMA transmission.

[0062] The block diagram in Fig. 4 describes the Multi-Carrier Block-Spread (MCBS) CDMA downlink transmission scheme (where only the m -th user is explicitly shown), that transforms the M user data symbol sequences

$$\{s^m[i]\}_{m=1}^M$$

into the multi-user chip sequence $u[n]$ with a rate $1/T_c$. Apart from the user multiplexing and the IFFT, the transmission scheme performs three major operations, namely linear precoding, block spreading, and adding transmit redundancy. Since our scheme belongs to the general class of block transmission schemes, the m -th user's data symbol sequence $s^m[i]$ is first serial-to-parallel converted into blocks of B symbols, leading to the symbol block sequence

$$\mathbf{s}^m[i] := [s^m[iB], \dots, s^m[(i+1)B-1]]^T.$$

The blocks $\mathbf{s}^m[i]$ are linearly precoded by a $Q \times B$ matrix Θ to yield the $Q \times 1$ precoded symbol blocks:

$$\tilde{s}^m[i] := \Theta \cdot s^m[i] \quad (1)$$

where the linear precoding can be either redundant ($Q > B$) or non-redundant ($Q = B$). For conciseness, we limit our discussion to redundant precoding, but the proposed concepts apply equally well to non-redundant precoding. As we will show later, linear precoding guarantees symbol detection and maximum frequency-diversity gains, and thus robustifies the transmission against frequency-selective fading. Unlike the traditional approach of symbol spreading that operates on a single symbol, we apply here block spreading that operates on a block of symbols. Specifically, the block sequence $\tilde{s}^m[i]$ is spread by a factor N with the user composite code sequence $c^m[n]$, which is the multiplication of a short orthogonal Walsh-Hadamard spreading code that is MS specific and a long overlay scrambling code that is BS specific. The chip block sequences of the different active users are added, resulting into the multi-user chip block sequence :

$$\tilde{x}[n] = \sum_{m=1}^M \tilde{s}^m[i] c^m[n] \quad (2)$$

where the chip block index n is related to the symbol block index i by: $n = iN + n'$, $n' \in \{0, \dots, N-1\}$. As will become apparent later, block spreading enables MUI-resilient reception, and thus effectively deals with the MUI. Subsequently, the $Q \times Q$ IFFT matrix F_Q^H transforms the Frequency-Domain (FD) chip block sequence $\tilde{x}[n]$ into the Time-Domain (TD) chip block sequence:

$$x[n] = F_Q^H \cdot \tilde{x}[n] .$$

The $K \times Q$ transmit matrix T , with $K \geq Q$ adds some redundancy to the chip blocks $x[n]$: $u[n] := T \cdot x[n]$. As will be clarified later, this transmit redundancy copes with the time-dispersive effect of multi-path propagation and also enables low-complexity equalization at the receiver. Finally, the resulting transmitted chip block sequence $u[n]$ is parallel-to-serial converted into the corresponding scalar sequence

$$[u[nK], \dots, u[(n+1)K - 1]]^T := u[n]$$

and transmitted over the air at a rate $\frac{1}{T_c}$.

A.1.b Channel model.

[0063] Adopting a discrete-time baseband equivalent model, the chip-sampled received signal is a channel-distorted version of the transmitted signal, and can be written as:

$$v[n] = \sum_{l=0}^{L_c} h[l] u[n-l] + w[n], \quad (3)$$

where $h[l]$ is the chip-sampled FIR channel that models the frequency-selective multi-path propagation between the transmitter and the receiver including the effect of transmit and receive filters, L_c is the order of $h[l]$, and $w[n]$ denotes the additive gaussian noise, which we assume to be white with variance σ_w^2 . Furthermore, we define L as a known upperbound on the channel order: $L \geq L_c$, which can be well approximated by

$$L \approx \lfloor \frac{\tau_{max}}{T_c} \rfloor + 1,$$

where τ_{max} is the maximum delay spread within the given propagation environment.

A.1.c MUI-resilient reception.

[0064] The block diagram in Fig.5 describes the reception scheme for the MS of interest (which we assume to be the m -th one), which transforms the received sequence $v[n]$ into an estimate of the desired user's data symbol sequence $\hat{s}^m[j]$. Assuming perfect synchronization, the received sequence $v[n]$ is serial-to-parallel converted into its corresponding block sequence

$$v[n] := [v[nK], \dots, v[(n+1)K-1]]^T.$$

From the scalar input/output relationship in (3), we can derive the corresponding block input/output relationship :

$$v[n] = H[0] \cdot u[n] + H[1] \cdot u[n-1] + w[n], \quad (4)$$

where

$$w[n] := [w[nK], \dots, w[(n+1)K-1]]^T$$

is the noise block sequence, $H[0]$ is a $K \times K$ lower triangular Toeplitz matrix with entries

$$[H[0]]_{p,q} = h[p-q],$$

and $H[1]$ is a $K \times K$ upper triangular Toeplitz matrix with entries

$$[H[1]]_{p,q} = h[K+p-q].$$

The time-dispersive nature of multi-path propagation gives rise to so-called Inter-Block Interference (IBI) between successive blocks, which is modeled by the second term in (4). The $Q \times K$ receive matrix R again removes the redundancy from the blocks $v[n]$: $y[n] := R \cdot v[n]$. The purpose of the transmit/receive pair (T, R) is twofold. First, it allows for simple block by block processing by removing the IBI. Second, it enables low-complexity frequency-domain equalization by making the linear channel convolution to appear circulant to the received block. To guarantee perfect IBI removal, the pair (T, R) should satisfy the following condition :

$$R \cdot H[1] \cdot T = 0. \quad (5)$$

[0065] To enable circulant channel convolution, the resulting channel matrix $\hat{H} := R \cdot H[0] \cdot T$ should be circulant. In this way, we obtain a simplified block input/output relationship in the TD :

$$y[n] = \hat{H} \cdot x[n] + z[n], \quad (6)$$

where $z[n] := R \cdot w[n]$ is the corresponding noise block sequence. In general, two options for the pair (T, R) exist that satisfy the above conditions. The first option corresponds to Cyclic Prefixing (CP) in classical OFDM systems, and boils down to choosing $K=Q+L$, and selecting:

$$T = T_{cp} := [I_{cp}^T, I_L^T]^T, \quad R = R_{cp} := [0_{Q \times L}, I_Q], \quad (7)$$

where \mathbf{I}_{cp} consists of the last L rows of \mathbf{I}_Q . The circulant property is enforced at the transmitter by adding a cyclic prefix of length L to each block. Indeed, premultiplying a vector with \mathbf{T}_{cp} copies its last L entries and pastes them to its top. The IBI is removed at the receiver by discarding the cyclic prefix of each received block. Indeed, premultiplying a vector with \mathbf{R}_{cp} deletes its first L entries and thus satisfies (5).

5 [0066] The second option corresponds to Zero Padding (ZP), and boils down to setting $K=Q+L$, and selecting:

$$\mathbf{T} = \mathbf{T}_{zp} := \begin{bmatrix} \mathbf{I}_Q^T & \mathbf{0}_{Q \times L}^T \end{bmatrix}^T, \quad \mathbf{R} = \mathbf{R}_{zp} := \begin{bmatrix} \mathbf{I}_Q & \mathbf{I}_{zp} \end{bmatrix}, \quad (8)$$

10

where \mathbf{I}_{zp} is formed by the first L columns of \mathbf{I}_Q . Unlike classical OFDM systems, here the IBI is entirely dealt with at the transmitter. Indeed, premultiplying a vector with \mathbf{T}_{zp} pads L trailing zeros to its bottom, and thus satisfies (5). The circulant property is enforced at the receiver by time-aliasing each received block. Indeed, premultiplying a vector with \mathbf{R}_{zp} adds its last L entries to its first L entries.

15 [0067] Referring back to (6), circulant matrices possess a nice property that enables simple per-tone equalization in the frequency-domain.

Property 1 : Circulant matrices can be diagonalized by FFT operations:

20 [0068]

$$\hat{\mathbf{H}} = \mathbf{F}_Q^H \cdot \tilde{\mathbf{H}} \cdot \mathbf{F}_Q, \quad (9)$$

25

with

$$\tilde{\mathbf{H}} := \text{diag}(\tilde{\mathbf{h}}),$$

30

$$\tilde{\mathbf{h}} := [H(e^{j0}), H(e^{j\frac{2\pi}{Q}}), \dots, H(e^{j\frac{2\pi}{Q}(Q-1)})]$$

35 the FD channel response evaluated on the FFT grid,

$$H(z) := \sum_{l=0}^L h[l]z^{-l}$$

40 the z-transform of $h[l]$, and \mathbf{F}_Q the $Q \times Q$ FFT matrix.

[0069] Aiming at low-complexity FD processing, we transform $\mathbf{y}[n]$ into the FD by defining

$$\tilde{\mathbf{y}}[n] := \mathbf{F}_Q \cdot \mathbf{y}[n].$$

45

Relying on Property 1, this leads to the following FD block input/output relationship:

$$\tilde{\mathbf{y}}[n] = \tilde{\mathbf{H}} \cdot \tilde{\mathbf{x}}[n] + \tilde{\mathbf{z}}[n] \quad (10)$$

50

where

$$\tilde{\mathbf{z}}[n] := \mathbf{F}_Q \cdot \mathbf{z}[n]$$

55

is the corresponding FD noise block sequence. Stacking N consecutive chip blocks $\tilde{\mathbf{y}}[n]$ into

$$\tilde{\mathbf{Y}}[i] := [\tilde{y}[iN], \dots, \tilde{y}[(i+1)N-1]],$$

5 we obtain the symbol block level equivalent of (10):

$$\tilde{\mathbf{Y}}[i] = \tilde{\mathbf{H}} \cdot \tilde{\mathbf{X}}[i] + \tilde{\mathbf{Z}}[i], \quad (11)$$

10 where $\tilde{\mathbf{x}}[i]$ and $\tilde{\mathbf{z}}[i]$ are similarly defined as $\tilde{\mathbf{y}}[i]$. From (2) we also have that :

$$\tilde{\mathbf{X}}[i] = \sum_{m=1}^M \tilde{\mathbf{s}}^m[i] \cdot \mathbf{c}^m[i]^T, \quad (12)$$

15

where

$$\mathbf{c}^m[i] := [c^m[iN], \dots, c^m[(i+1)N-1]]^T$$

20

is the m -th user's composite code vector used to block spread its data symbol block $\mathbf{s}^m[i]$. By inspecting (11) and (12), we can conclude that our transceiver preserves the orthogonality among users, even after propagation through a (possibly unknown) frequency-selective multi-path channel. This property allows for deterministic MUI elimination through low-complexity code-matched filtering. Indeed, by block despreading (11) with the desired user's composite code vector $\mathbf{c}^m[i]$ (we assume the m -th user to be the desired one), we obtain :

25

$$\tilde{\mathbf{y}}^m[i] := \tilde{\mathbf{Y}}[i] \cdot \mathbf{c}^m[i]^* = \tilde{\mathbf{H}} \cdot \Theta \cdot \mathbf{s}^m[i] + \tilde{\mathbf{z}}^m[i], \quad (13)$$

30

where

35

$$\tilde{\mathbf{z}}^m[i] := \tilde{\mathbf{Z}}[i] \cdot \mathbf{c}^m[i]^*$$

is the corresponding noise block sequence. Our transceiver successfully converts (through block despreading) a multi-user chip block equalization problem into an equivalent single-user symbol block equalization problem. Moreover, the operation of block despreading preserves Maximum-Likelihood (ML) optimality, since it does not incur any information loss regarding the desired user's symbol block $\mathbf{s}^m[i]$.

40

A.1.d Single-user equalization.

45 **[0070]** After successful elimination of the MUI, we still need to detect the desired user's symbol block $\mathbf{s}^m[i]$ from (13). Ignoring for the moment the presence of Θ (or equivalently setting $Q=B$ and selecting $\Theta=\mathbf{I}_Q$), this requires $\tilde{\mathbf{H}}$ to have full column rank Q . Unfortunately, this condition only holds for channels that do not invoke any zero diagonal entries in $\tilde{\mathbf{H}}$. In other words, if the MS experiences a deep channel fade on a particular tone (corresponding to a zero diagonal entry in $\tilde{\mathbf{H}}$), the information symbol on that tone can not be recovered. To guarantee symbol detectability of the B symbols in $\mathbf{s}^m[i]$, regardless of the symbol constellation, we thus need to design the precoder Θ such that:

50

$$\text{rank}(\tilde{\mathbf{H}} \cdot \Theta) = B, \quad (14)$$

55

irrespective of the underlying channel realization. Since an FIR channel of order L can invoke at most L zero diagonal entries in $\tilde{\mathbf{H}}$, this requires any $Q-L=B$ rows of Θ to be linearly independent. Two classes of precoders have been constructed that satisfy this condition and thus guarantee symbol detectability or equivalently enable full frequency-

diversity gain, namely the Vandermonde precoders and the cosine precoders. For instance, a special case of the general cosine precoder is a truncated Discrete Cosine Transform (DCT) matrix.

A.2. Equalization options

[0071] In this section, we discuss different options to perform equalization and decoding of the linear precoding, either jointly or separately. These options allow to trade-off performance versus complexity, ranging from optimal Maximum-Likelihood (ML) detection with exponential complexity to linear and decision-directed detection with linear complexity. To evaluate the complexity, we distinguish between the initialization phase, where the equalizers are calculated, and the data processing phase, where the actual equalization takes place. The rate of the former is related to the channel's fading rate, whereas the latter is executed continuously at the symbol block rate.

A.2.a ML detection

[0072] The ML algorithm is optimal in a Maximum Likelihood sense, but has a very high complexity. The likelihood function of the received block $\tilde{\mathbf{y}}^m[i]$, conditioned on the transmitted block $\mathbf{s}^m[i]$, is given by :

$$p(\tilde{\mathbf{y}}^m[i] | \mathbf{s}^m[i]) = \frac{1}{(\pi\sigma_w^2)^Q} \exp \left(-\frac{\|\tilde{\mathbf{y}}^m[i] - \tilde{\mathbf{H}} \cdot \Theta \cdot \mathbf{s}^m[i]\|^2}{\sigma_w^2} \right). \quad (15)$$

Amongst all possible transmitted blocks, the ML algorithm retains the one that maximizes the likelihood function or, equivalently, minimizes the Euclidean distance:

$$\hat{\mathbf{s}}^m[i] = \arg \min_{\mathbf{s}^m[i] \in S} \|\tilde{\mathbf{y}}^m[i] - \tilde{\mathbf{H}} \cdot \Theta \cdot \mathbf{s}^m[i]\|^2. \quad (16)$$

[0073] In other words, the ML metric is given by the Euclidean distance between the actual received block and the block that would have been received if a particular symbol block had been transmitted in a noiseless environment. The number of possible transmit vectors in S is the cardinality of S , i.e. $|S|=M^B$, with M the constellation size. So, the number of points to inspect during the data processing phase grows exponentially with the initial block length B . Hence, this algorithm is only feasible for a small block length B and a small constellation size M . Note that the ML algorithm does not require an initialization phase.

A.2.b Joint Linear Equalization and Decoding.

[0074] Linear equalizers that perform joint equalization and decoding combine a low complexity with medium performance. A first possibility is to apply a Zero-Forcing (ZF) linear equalizer :

$$\mathbf{G}_{ZF} = (\Theta^H \cdot \tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} \cdot \Theta)^{-1} \cdot \Theta^H \cdot \tilde{\mathbf{H}}^H, \quad (17)$$

which completely eliminates the ISI, irrespective of the noise level. By ignoring the noise, it causes excessive noise enhancement, especially at low SNR. A second possibility is to apply a Minimum Mean-Square-Error (MMSE) linear equalizer :

$$\mathbf{G}_{MMSE} = \left(\Theta^H \cdot \tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} \cdot \Theta + \frac{\sigma_w^2}{\sigma_s^2} \mathbf{I}_B \right)^{-1} \cdot \Theta^H \cdot \tilde{\mathbf{H}}^H, \quad (18)$$

which minimizes the MSE between the actual transmitted symbol block and its estimate. The MMSE linear equalizer explicitly takes into account the noise variance σ_w^2 and the information symbol variance σ_s^2 , and balances ISI elimination with noise enhancement. From (17) and (18), it is also clear that \mathbf{G}_{MMSE} reduces to \mathbf{G}_{ZF} at high SNR.

[0075] During the initialization phase, \mathbf{G}_{ZF} and \mathbf{G}_{MMSE} can be computed from the multiple sets of linear equations, implicitly shown in (17) and (18), respectively. The solution can be found from Gaussian elimination with partial pivoting, based on the LU decomposition, leading to an overall complexity of $O(QB^2)$. During the data processing phase, the equalizers \mathbf{G}_{ZF} and \mathbf{G}_{MMSE} are applied to the received block $\tilde{\mathbf{y}}^m[i]$, leading to a complexity of $O(QB)$.

A.2.c Joint Decision Feedback Equalization and Decoding.

[0076] On the one hand, the ML algorithm of Subsection A.2.a achieves the optimal performance but with a very high complexity. On the other hand, the linear equalizers of Subsection A.2.b offer a low complexity but at a relatively poor performance. The class of non-linear equalizers that perform joint decision feedback equalization and decoding lie in between the former categories, both in terms of performance and complexity. Decision feedback equalizers exploit the finite alphabet property of the information symbols to improve performance relative to linear equalizers. They consist of a feedforward section, represented by the matrix \mathbf{W} , and a feedback section, represented by the matrix \mathbf{B} :

$$\hat{\mathbf{s}}^m[i] = \text{slice} \left[\mathbf{W} \cdot \tilde{\mathbf{y}}^m[i] - \mathbf{B} \cdot \hat{\mathbf{s}}^m[i] \right]. \quad (19)$$

[0077] The feedforward and feedback section can be designed according to a ZF or MMSE criterium. In either case, \mathbf{B} should be a strictly upper or lower triangular matrix with zero diagonal entries, in order to feedback decisions in a causal way. To design the decision feedback counterpart of the ZF linear equalizer, we compute the Cholesky decomposition of the matrix

$$\mathbf{\Theta}^H \cdot \tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} \cdot \mathbf{\Theta}$$

in (17):

$$\mathbf{\Theta}^H \cdot \tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} \cdot \mathbf{\Theta} = (\mathbf{\Sigma}_1 \cdot \mathbf{U}_1)^H \cdot \mathbf{\Sigma}_1 \cdot \mathbf{U}_1, \quad (20)$$

where \mathbf{U}_1 is an upper triangular matrix with ones along the diagonal, and $\mathbf{\Sigma}_1$ is a diagonal matrix with real entries. The ZF feedforward and feedback matrices then follow from:

$$\mathbf{W}_{ZF} = \mathbf{U}_1 \cdot \mathbf{G}_{ZF} = \mathbf{\Sigma}_1^{-1} \cdot (\mathbf{U}_1^H \cdot \mathbf{\Sigma}_1)^{-1} \cdot \mathbf{\Theta}^H \cdot \tilde{\mathbf{H}}^H, \quad \mathbf{B}_{ZF} = \mathbf{U}_1 - \mathbf{I}_B. \quad (21)$$

[0078] The linear feedforward section \mathbf{W}_{ZF} suppresses the ISI originating from "future" symbols, the so-called precursor ISI, whereas the non-linear feedback section \mathbf{B}_{ZF} eliminates the ISI originating from "past" symbols, the so-called post-cursor ISI.

[0079] Likewise, to design the decision feedback counterpart of the MMSE linear equalizer, we compute the Cholesky decomposition of the matrix

$$\mathbf{\Theta}^H \cdot \tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} \cdot \mathbf{\Theta} + \frac{\sigma_w^2}{\sigma_s^2} \mathbf{I}_B$$

in (18):

$$\mathbf{\Theta}^H \cdot \tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} \cdot \mathbf{\Theta} + \frac{\sigma_w^2}{\sigma_s^2} \mathbf{I}_B = (\mathbf{\Sigma}_2 \cdot \mathbf{U}_2)^H \cdot \mathbf{\Sigma}_2 \cdot \mathbf{U}_2, \quad (22)$$

where \mathbf{U}_2 is an upper triangular matrix with ones along the diagonal, and $\mathbf{\Sigma}_2$ is a diagonal matrix with real entries. The

MMSE feedforward and feedback matrices can then be calculated as :

$$\mathbf{W}_{MMSE} = \mathbf{U}_2 \cdot \mathbf{G}_{MMSE} = \Sigma_2^{-1} \cdot (\mathbf{U}_2^H \cdot \Sigma_2)^{-1} \cdot \Theta^H \cdot \tilde{\mathbf{H}}^H, \quad \mathbf{B}_{MMSE} = \mathbf{U}_2 - \mathbf{I}_B. \quad (23)$$

[0080] During the initialization phase, the feedforward and feedback filters are computed based on a Cholesky decomposition, leading to an overall complexity of $O(QB^2)$. During the data processing phase, the feedforward and feedback filters are applied to the received data according to (19), leading to a complexity of $O(QB)$. Note that the decision feedback equalizers involve the same order of complexity as their linear counterparts.

A.2.d Separate Linear Equalization and Decoding.

[0081] Previously, we have only considered joint equalization and decoding of the linear precoding. However, in order to even further reduce the complexity with respect to the linear equalizers of Subsection A.2.b, equalization and decoding can be performed separately as well :

$$\hat{\mathbf{s}}^m[i] = \Theta^H \cdot \tilde{\mathbf{G}} \cdot \tilde{\mathbf{y}}^m[i], \quad (24)$$

where $\tilde{\mathbf{G}}$ performs linear equalization only and tries to restore $\tilde{\mathbf{s}}^m[i]$, and Θ^H subsequently performs linear decoding only and tries to restore $\mathbf{s}^m[i]$.

[0082] The ZF equalizer perfectly removes the amplitude and phase distortion:

$$\tilde{\mathbf{G}}_{ZF} = (\tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}})^{-1} \cdot \tilde{\mathbf{H}}^H, \quad (25)$$

but also causes excessive noise enhancement, especially on those tones that experience a deep channel fade. Since $\tilde{\mathbf{H}}$ is a diagonal matrix, the ZF equalizer decouples into Q parallel single-tap equalizers, acting on a per-tone basis in the FD. The MMSE equalizer balances amplitude and phase distortion with noise enhancement and can be expressed as :

$$\tilde{\mathbf{G}}_{MMSE} = (\tilde{\mathbf{H}}^H \cdot \tilde{\mathbf{H}} + \sigma_w^2 \mathbf{R}_i^{-1})^{-1} \cdot \tilde{\mathbf{H}}^H, \quad (26)$$

where

$$\mathbf{R}_i := \mathbb{E}\{\tilde{\mathbf{s}}^m[i] \cdot \tilde{\mathbf{s}}^m[i]^H\} = \sigma_s^2 \Theta \cdot \Theta^H$$

is the covariance matrix of $\tilde{\mathbf{s}}^m[i]$. If we neglect the color in the precoded symbols

$$\mathbf{R}_i \approx \sigma_s^2 \mathbf{I}_Q,$$

the MMSE equalizer also decouples into Q parallel and independent single-tap equalizers.

[0083] During the initialization phase, $\tilde{\mathbf{G}}_{ZF}$ and $\tilde{\mathbf{G}}_{MMSE}$ are calculated from (25) and (26), respectively, where the matrix inversion reduces to Q parallel scalar divisions, leading to an overall complexity of $O(Q)$. During the data processing phase, the received data is separately equalized and decoded, leading to an overall complexity of $O(QB)$.

A.3 Extension to multiple antennas.

[0084] As showed in Sections A.1 and A.2, MCBS-CDMA successfully addresses the challenges of broadband cellular downlink communications. However, the spectral efficiency of single-antenna MCBS-CDMA is still limited by the

received signal-to-noise ratio and cannot be further improved by traditional communication techniques. As opposed to single-antenna systems, Multiple-Input Multiple-Output (MIMO) systems that deploy N_T transmit and N_R receive antennas, enable a N_{min} -fold capacity increase in rich scattering environments, where $N_{min} = \min\{N_T, N_R\}$ is called the multiplexing gain. Besides the time, frequency and code dimensions, MIMO systems create an extra spatial dimension that allows to increase the spectral efficiency and/or to improve the performance. On the one hand, Space Division Multiplexing (SDM) techniques achieve high spectral efficiency by exploiting the spatial multiplexing gain. On the other hand, Space-Time Coding (STC) techniques achieve high Quality-of-Service (QoS) by exploiting diversity and coding gains. Besides the leverages they offer, MIMO systems also sharpen the challenges of broadband cellular downlink communications. First, time dispersion and ISI are now caused by $N_T N_R$ frequency-selective multi-path fading channels instead of just 1. Second, MUI originates from $N_T M$ sources instead of just M . Third, the presence of multiple antennas seriously impairs a low-complexity implementation of the MS. To tackle these challenges, we will demonstrate the synergy between our MCBS-CDMA waveform and MIMO signal processing. In particular, we focus on a space-time block coded MCBS-CDMA transmission, but the general principles apply equally well to a space-time trellis coded or a space division multiplexed MCBS-CDMA transmission.

A.3.a Space-time block coded MCBS-CDMA transmission.

[0085] The block diagram in Fig. 6 describes the Space-Time Block Coded (STBC) MCBS-CDMA downlink transmission scheme (where only the m -th user is explicitly shown), that transforms the M user data symbol sequences

$$\{s^m[i]\}_{m=1}^M$$

into N_T ST coded multi-user chip sequences

$$\{u_{n_i}[n]\}_{n_i=1}^{N_T}$$

with a rate $\frac{1}{T_c}$. For conciseness, we limit ourselves to the case of $N_T = 2$ transmit antennas. As for the single-antenna case, the information symbols are first grouped into blocks of B symbols and linearly precoded. Unlike the traditional approach of performing ST encoding at the *scalar* symbol level, we perform ST encoding at the symbol *block* level. Our ST encoder operates in the FD and takes two consecutive symbol blocks

$$\{\bar{s}^m[2i], \bar{s}^m[2i+1]\}$$

to output the following $2Q \times 2$ matrix of ST coded symbol blocks :

$$\begin{bmatrix} \bar{s}_1^m[2i] & \bar{s}_1^m[2i+1] \\ \bar{s}_2^m[2i] & \bar{s}_2^m[2i+1] \end{bmatrix} = \begin{bmatrix} \bar{s}^m[2i] & -\bar{s}^m[2i+1]^* \\ \bar{s}^m[2i+1] & \bar{s}^m[2i]^* \end{bmatrix}. \quad (27)$$

[0086] At each time interval i , the ST coded symbol blocks $\bar{s}_1^m[i]$ and $\bar{s}_2^m[i]$ are forwarded to the first and the second transmit antenna, respectively. From (27), we can easily verify that the transmitted symbol block at time instant $2i+1$ from one antenna is the conjugate of the transmitted symbol block at time instant $2i$ from the other antenna (with a possible sign change). This corresponds to a per-tone implementation of the classical Alamouti scheme for frequency-flat fading channels. As we will show later, this property allows for deterministic transmit stream separation at the receiver.

[0087] After ST encoding, the resulting symbol block sequences $\{\bar{s}_{n_i}^m[i]\}_{n_i=1}^{N_T}$ are block spread and code division multiplexed with those of the other users :

$$\tilde{\mathbf{x}}_{n_i}[n] = \sum_{m=1}^M \tilde{\mathbf{s}}_{n_i}^m[i] c^m[n], \quad n = iN + n'. \quad (28)$$

[0088] At this point, it is important to note that each of the N_T parallel block sequences are block spread by the *same* composite code sequence $c^m[n]$, guaranteeing an efficient utilization of the available code space. As will become apparent later, this property allows for deterministic user separation at every receive antenna. After IFFT transformation and the addition of some form of transmit redundancy :

$$\mathbf{u}_{n_i}[n] = \mathbf{T} \cdot \mathbf{F}_Q^H \cdot \tilde{\mathbf{x}}_{n_i}[n], \quad (29)$$

the corresponding scalar sequences

$$\{u_{n_i}[n]\}_{n_i=1}^{N_T}$$

are transmitted over the air at a rate $\frac{1}{T_c}$.

A.3.B MUI-resilient MIMO reception.

[0089] The block diagram in Fig. 7 describes the reception scheme for the MS of interest, which transforms the different received sequences $\{v_{n_i}[n]\}_{n_i=1}^{N_T}$ into an estimate of the desired user's data sequence $s^m[i]$. After transmit redundancy removal and FFT transformation, we obtain the multi-antenna counterpart of (11) :

$$\tilde{\mathbf{Y}}_{n_i}[i] = \sum_{n_i=1}^{N_T} \tilde{\mathbf{H}}_{n_i, n_i} \cdot \tilde{\mathbf{X}}_{n_i}[i] + \tilde{\mathbf{Z}}_{n_i}[i], \quad (30)$$

where

$$\tilde{\mathbf{Y}}_{n_i}[i] := [\tilde{y}_{n_i}[iN], \dots, \tilde{y}_{n_i}[(i+1)N-1]]$$

stacks N consecutive received chip blocks $\tilde{y}_{n_i}[n]$ at the n_i -th receive antenna, $\tilde{\mathbf{H}}_{n_i, n_i}[i]$ is the diagonal FD channel matrix from the n_i -th transmit to the n_i -th receive antenna, and $\tilde{\mathbf{x}}_{n_i}[i]$ and $\tilde{\mathbf{z}}_{n_i}[i]$ are similarly defined as $\tilde{y}_{n_i}[i]$. From (28) and (30), we can conclude that our transceiver retains the user orthogonality at each receive antenna, irrespective of the underlying frequency-selective multi-path channels. Like in the single-antenna case, a low-complexity block despreading operation with the desired user's composite code vector $c^m[i]$ deterministically removes the MUI at each receive antenna :

$$\tilde{\mathbf{y}}_{n_i}^m[i] := \tilde{\mathbf{Y}}_{n_i}[i] \cdot \mathbf{c}^m[i]^* = \sum_{n_i=1}^{N_T} \tilde{\mathbf{H}}_{n_i, n_i} \cdot \tilde{\mathbf{s}}_{n_i}^m[i] + \tilde{\mathbf{z}}_{n_i}^m[i], \quad (31)$$

[0090] Hence, our transceiver successfully converts (through block despreading) a multi-user MIMO detection problem into an equivalent single-user MIMO equalization problem.

A.3.c Single-user space-time decoding.

[0091] After MUI elimination, the information blocks $\mathbf{s}^m[i]$ still need to be decoded from the received block despread

sequences $\{\bar{\mathbf{y}}_{n_r}^m[l]\}_{l=1}^{N_R}$. Our ST decoder decomposes into three steps: an initial ST decoding step and a transmit stream separation step for each receive antenna, and, finally, a receive antenna combining step.

[0092] The initial ST decoding step considers two consecutive symbol blocks $\{\bar{\mathbf{y}}_{n_r}^m[2i]$ and $\bar{\mathbf{y}}_{n_r}^m[2i+1]\}$, both satisfying the block input/output relationship of (31). By exploiting the ST code structure of (27), we arrive at :

$$\bar{\mathbf{y}}_{n_r}^m[2i] = \tilde{\mathbf{H}}_{n_r,1} \cdot \bar{\mathbf{s}}_1^m[2i] + \tilde{\mathbf{H}}_{n_r,2} \cdot \bar{\mathbf{s}}_2^m[2i] + \bar{\mathbf{z}}_{n_r}^m[2i], \quad (32)$$

$$\bar{\mathbf{y}}_{n_r}^m[2i+1]^* = -\tilde{\mathbf{H}}_{n_r,1}^* \cdot \bar{\mathbf{s}}_2^m[2i] + \tilde{\mathbf{H}}_{n_r,2}^* \cdot \bar{\mathbf{s}}_1^m[2i] + \bar{\mathbf{z}}_{n_r}^m[2i+1]^*. \quad (33)$$

[0093] Combining (6) and (7) into a single block matrix form, we obtain :

$$\underbrace{\begin{bmatrix} \bar{\mathbf{y}}_{n_r}^m[2i] \\ \bar{\mathbf{y}}_{n_r}^m[2i+1]^* \end{bmatrix}}_{\bar{\mathbf{y}}_{n_r}^m[i]} = \underbrace{\begin{bmatrix} \tilde{\mathbf{H}}_{n_r,1} & \tilde{\mathbf{H}}_{n_r,2} \\ \tilde{\mathbf{H}}_{n_r,2}^* & -\tilde{\mathbf{H}}_{n_r,1}^* \end{bmatrix}}_{\bar{\mathbf{H}}_{n_r}} \cdot \underbrace{\begin{bmatrix} \bar{\mathbf{s}}^m[2i] \\ \bar{\mathbf{s}}^m[2i+1] \end{bmatrix}}_{\bar{\mathbf{s}}_{n_r}^m[i]} + \underbrace{\begin{bmatrix} \bar{\mathbf{z}}_{n_r}^m[2i] \\ \bar{\mathbf{z}}_{n_r}^m[2i+1]^* \end{bmatrix}}_{\bar{\mathbf{z}}_{n_r}^m[i]}, \quad (34)$$

where

$$\bar{\mathbf{s}}_1^m[2i] = \bar{\mathbf{s}}^m[2i]$$

and

$$\bar{\mathbf{s}}_2^m[2i] = \bar{\mathbf{s}}^m[2i+1]$$

follow from (27). From the structure of $\bar{\mathbf{H}}_{n_r}$ in (34), we can deduce that our transceiver retains the orthogonality among transmit streams at each receive antenna for each tone separately, regardless of the underlying frequency-selective multi-path channels. A similar property was also encountered in the classical Alamouti scheme, but only for single-user frequency-flat fading multi-path channels.

[0094] The transmit stream separation step relies on this property to deterministically remove the transmit stream interference through low-complexity linear processing. Let us define the $Q \times Q$ matrix $\tilde{\mathbf{D}}_{n_r}$ with non-negative diagonal entries as:

$$\tilde{\mathbf{D}}_{n_r} := [\tilde{\mathbf{H}}_{n_r,1} \cdot \tilde{\mathbf{H}}_{n_r,1}^* + \tilde{\mathbf{H}}_{n_r,2} \cdot \tilde{\mathbf{H}}_{n_r,2}^*]^{1/2}.$$

From (34), we can verify that the channel matrix $\bar{\mathbf{H}}_{n_r}$ satisfies:

$$\bar{\mathbf{H}}_{n_r}^H \cdot \bar{\mathbf{H}}_{n_r} = \mathbf{I}_2 \otimes \tilde{\mathbf{D}}_{n_r}^2,$$

where \otimes stands for Kronecker product. Based on $\bar{\mathbf{H}}_{n_r}$ and $\tilde{\mathbf{D}}_{n_r}$, we can construct a unitary matrix

$$\bar{\mathbf{U}}_{n_r} := \bar{\mathbf{H}}_{n_r} \cdot (\mathbf{I}_2 \otimes \tilde{\mathbf{D}}_{n_r}^{-1}),$$

which satisfies $\bar{\mathbf{U}}_{n_r}^H \cdot \bar{\mathbf{U}}_{n_r} = \mathbf{I}_{2Q}$ and

$$\bar{\mathbf{U}}_{n_r}^H \cdot \bar{\mathbf{H}}_{n_r} = \mathbf{I}_2 \otimes \tilde{\mathbf{D}}_{n_r}.$$

Performing unitary combining on (34) (through $\bar{\mathbf{U}}_{n_r}^H$), collects the transmit antenna diversity at the n_r -th receive antenna :

$$\underbrace{\begin{bmatrix} \hat{\mathbf{y}}_{n_r}^m[2i] \\ \hat{\mathbf{y}}_{n_r}^m[2i+1] \end{bmatrix}}_{\mathbf{r}_{n_r}^m[i]} := \bar{\mathbf{U}}_{n_r}^H \cdot \bar{\mathbf{r}}_{n_r}^m[i] = \begin{bmatrix} \tilde{\mathbf{D}}_{n_r} \cdot \tilde{\mathbf{s}}^m[2i] \\ \tilde{\mathbf{D}}_{n_r} \cdot \tilde{\mathbf{s}}^m[2i+1] \end{bmatrix} + \underbrace{\begin{bmatrix} \hat{\mathbf{z}}_{n_r}^m[2i] \\ \hat{\mathbf{z}}_{n_r}^m[2i+1] \end{bmatrix}}_{\mathbf{r}_{n_r}^m[i]}, \quad (35)$$

where the resulting noise $\mathbf{r}_{n_r}^m[i] := \bar{\mathbf{U}}_{n_r}^H \cdot \mathbf{r}_{n_r}^m[i]$ is still white with variance σ^2 . Since multiplying with a unitary matrix preserves ML optimality, we can deduce from (35) that the symbol blocks $\tilde{\mathbf{s}}^m[2i]$ and $\tilde{\mathbf{s}}^m[2i+1]$ can be decoded separately in an optimal way. As a result, the different symbol blocks $\tilde{\mathbf{s}}^m[i]$ can be detected independently from :

$$\hat{\mathbf{y}}_{n_r}^m[i] = \tilde{\mathbf{D}}_{n_r} \cdot \tilde{\mathbf{s}}^m[i] + \hat{\mathbf{z}}_{n_r}^m[i]. \quad (36)$$

[0095] Stacking the blocks from the different receive antennas

$$\{\hat{\mathbf{y}}_{n_r}^m[i]\}_{n_r=1}^{N_R}$$

for the final receive antenna combining step, we obtain :

$$\underbrace{\begin{bmatrix} \hat{\mathbf{y}}_1^m[i] \\ \vdots \\ \hat{\mathbf{y}}_{N_R}^m[i] \end{bmatrix}}_{\mathbf{y}^m[i]} = \underbrace{\begin{bmatrix} \tilde{\mathbf{D}}_1 \\ \vdots \\ \tilde{\mathbf{D}}_{N_R} \end{bmatrix}}_{\mathbf{H}} \cdot \tilde{\mathbf{s}}^m[i] + \underbrace{\begin{bmatrix} \hat{\mathbf{z}}_1^m[i] \\ \vdots \\ \hat{\mathbf{z}}_{N_R}^m[i] \end{bmatrix}}_{\mathbf{z}^m[i]} \quad (37)$$

[0096] At this point, we have only collected the transmit antenna diversity at each receive antenna, but still need to collect the receive antenna diversity. Let us define the $Q \times Q$ matrix $\tilde{\mathbf{D}}$ with non-negative diagonal entries as:

$$\tilde{\mathbf{D}} := \left[\sum_{n_t=1}^{N_T} \sum_{n_r=1}^{N_R} \tilde{\mathbf{H}}_{n_r, n_t} \cdot \tilde{\mathbf{H}}_{n_r, n_t}^* \right]^{1/2}.$$

From (37), we can verify that:

$$\mathbf{H}^H \cdot \mathbf{H} = \tilde{\mathbf{D}}^2.$$

Based on \mathbf{H} and $\tilde{\mathbf{D}}$, we can construct a tall unitary matrix

$$\hat{\mathbf{U}} := \mathbf{H} \cdot \tilde{\mathbf{D}}^{-1},$$

which satisfies $\hat{\mathbf{U}}^H \hat{\mathbf{U}} = \mathbf{I}_Q$ and

$$\hat{\mathbf{U}}^H \cdot \mathbf{H} = \tilde{\mathbf{D}}.$$

Gathering the receive antenna diversity through multiplying (11) with $\hat{\mathbf{U}}^H$, we finally obtain :

$$\tilde{\mathbf{y}}^m[i] := \hat{\mathbf{U}}^H \cdot \hat{\mathbf{y}}^m[i] = \tilde{\mathbf{D}} \cdot \Theta \cdot \mathbf{s}^m[i] + \tilde{\mathbf{z}}^m[i], \quad (38)$$

where the resulting noise

$$\tilde{\mathbf{z}}^m[i] := \hat{\mathbf{U}}^H \cdot \mathbf{z}^m[i]$$

is still white with variance σ_z^2 . Since the multiplication with a tall unitary matrix that does not remove information also preserves ML decoding optimality, the blocks $\mathbf{s}^m[i]$ can be optimally decoded from (38). Moreover, (38) has the same structure as its single-antenna counterpart in (13). Hence, the design of the linear precoder Θ in Subsection A.1.d, and the different equalization options that we have discussed in Section A.2, can be applied here as well.

A.4 Conclusion.

[0097] To cope with the challenges of broadband cellular downlink communications, we have designed a novel Multi-Carrier (MC) CDMA transceiver that enables significant performance improvements compared to 3G cellular systems, yielding gains of up to 9 dB in full load situations. To this end, our so-called Multi-Carrier Block-Spread (MCBS) CDMA transceiver capitalizes on redundant block-spreading and linear precoding to preserve the orthogonality among users and to enable full multi-path diversity gains, regardless of the underlying multi-path channels. Different equalization options, ranging from linear to ML detection, strike the trade-off between performance and complexity. Specifically, the MMSE decision feedback equalizer realizes a 2.8 dB gain relative to its linear counterpart and performs within 1.4 dB of the optimal ML detector. Finally, our transceiver demonstrates a rewarding synergy with multi-antenna techniques to increase the spectral efficiency and/or improve the link reliability over MIMO channels. Specifically, our STBC/MCBS-CDMA transceiver retains the orthogonality among users as well as transmit streams to realize both multi-antenna and multi-path diversity gains of $N_T N_R (L_c + 1)$ for every user in the system, irrespective of the system load. Moreover, a low-complexity linear MMSE detector, that performs either joint or separate equalization and decoding, approaches the optimal ML performance (within 0.4 dB for a (2,2) system) and comes close to extracting the full diversity in reduced as well as full load settings.

B. Embodiment.

B.1. MC-DS-CDMA downlink system model.

B.1.a. Transmitter model.

[0098] Let us consider the downlink of a single-cell space-time coded MC-DS-CDMA system with U active mobile stations. As depicted in Figure 8, at the base-station, which we suppose to have M_t transmit antennas, a space-time coded MC-DS-CDMA transmission scheme transforms the different user symbol sequences

$$\{\mathbf{s}^u[i]\}_{u=1}^U$$

and the pilot symbol sequence $\mathbf{s}^p[i]$ into M_t time-domain space-time coded multi-user chip sequences

$$\{\mathbf{u}_{m_t}[n]\}_{m_t=1}^{M_t},$$

where $u_{m_t}[n]$ is transmitted from the m_t -th transmit antenna. For simplicity reasons, we will assume in the following that the base-station has only $M_t=2$ transmit antennas. Note however that the proposed techniques can be extended to the more general case of $M_t > 2$ transmit antennas when resorting to the generalized orthogonal designs. As shown in Figure 8, each user's data symbol sequence $s^u[i]$ (similar for the pilot symbol sequence $p^u[i]$) is serial-to-parallel converted into blocks of B symbols, leading to the symbol block sequence

$$s^u[i] := [s^u[iB] \quad \dots \quad s^u[(i+1)B-1]]^T.$$

The symbol block sequence $s^u[i]$ is linearly precoded by a $Q \times B$ matrix Θ , with Q the number of tones, to yield the precoded symbol block sequence

$$\tilde{s}^u[i] := \Theta \cdot s^u[i].$$

The precoded symbol block sequence $\tilde{s}^u[i]$ is demultiplexed into M_t parallel sequences

$$\{\tilde{s}_{m_t}^u[i] := \tilde{s}^u[iM_t + m_t - 1]\}_{m_t=1}^{M_t},$$

where M_t is the number of transmit antennas. Each of the u -th user's precoded symbol block sequences

$$\{\tilde{s}_{m_t}^u[i]\}_{m_t=1}^{M_t}$$

is spread by a factor N with the same user code sequence $c_u[n]$, which is the multiplication of the user specific orthogonal Walsh-Hadamard spreading code and the base-station specific scrambling code. For each of the M_t parallel streams, the different user chip block sequences are added up together with the pilot chip block sequence, resulting into the m_t -th multi-user chip block sequence :

$$\tilde{x}_{m_t}[n] = \sum_{u=1}^U \tilde{s}_{m_t}^u[i] c_u[n] + \tilde{s}_{m_t}^p[i] c_p[n] \quad (39)$$

with $i = \frac{n}{N}$. The Space-Time (ST) encoder operates in the frequency-domain and takes the two multi-user chip blocks

$$\{\tilde{x}_{m_t}[n]\}_{m_t=1}^2$$

to output the following $2Q \times 2$ matrix of ST coded multi-user chip blocks :

$$\begin{bmatrix} \tilde{x}_1[2n] & \tilde{x}_1[2n+1] \\ \tilde{x}_2[2n] & \tilde{x}_2[2n+1] \end{bmatrix} = \begin{bmatrix} \tilde{x}_1[n] & -\tilde{x}_2^*[n] \\ \tilde{x}_2[n] & \tilde{x}_1^*[n] \end{bmatrix} \quad (40)$$

[0099] At each time interval n , the ST coded multi-user chip blocks $\tilde{x}_1[n]$ and $\tilde{x}_2[n]$ are forwarded to the first respectively the second transmit antenna. From Equation 40, we can easily verify that the transmitted multi-user chip block at time instant $2n+1$ from one antenna is the conjugate of the transmitted multi-user chip block at time instant $2n$ from the other antenna. The $Q \times Q$ IFFT matrix F_Q^H transforms the frequency-domain ST coded multi-user chip block sequence $\tilde{x}_{m_t}[n]$ into the time-domain ST coded multi-user chip block sequence $x_{m_t}[n] = F_Q^H \cdot \tilde{x}_{m_t}[n]$. The $K \times Q$ transmit matrix T , with $K=Q+\mu$, adds a cyclic prefix of length μ to each block of the time-domain ST coded multi-user chip block

sequence $\mathbf{x}_{m_l}[n]$ leading to the time-domain transmitted multi-user chip block sequence $\mathbf{u}_{m_l}[n] = \mathbf{T} \cdot \mathbf{x}_{m_l}[n]$. Finally, the time-domain transmitted multi-user chip block sequence $\mathbf{u}_{m_l}[n]$ is parallel-to-serial converted into K chips, obtaining the time-domain transmitted multi-user chip sequence

$$\begin{bmatrix} u_{m_l}[nK] & \dots & u_{m_l}[(n+1)K-1] \end{bmatrix}^T := \mathbf{u}_{m_l}[n].$$

B.1.b Receiver model.

[0100] One assumes that the mobile station of interest is equipped with M_r receive antennas and has acquired perfect synchronisation. As shown in Figure 9, at each receive antenna, the time-domain received chip sequence $v_{m_r}[n]$ is serial-to-parallel converted into blocks of K chips, resulting into the time-domain received chip block sequence

$$\mathbf{v}_{m_r}[n] := \begin{bmatrix} v_{m_r}[nK] & \dots & v_{m_r}[(n+1)K-1] \end{bmatrix}^T.$$

The $Q \times K$ receive matrix \mathbf{R} discards the cyclic prefix of each block of the time-domain received chip block sequence $\mathbf{v}_{m_r}[n]$ leading to the time-domain received ST coded chip block sequence $\mathbf{y}_{m_r}[n] = \mathbf{R} \cdot \mathbf{v}_{m_r}[n]$. By transforming the time-domain received ST coded chip block sequence $\mathbf{y}_{m_r}[n]$ into the frequency-domain $\bar{\mathbf{y}}_{m_r}[n] := \mathbf{F}_Q \cdot \mathbf{y}_{m_r}[n]$ with the $Q \times Q$ FFT matrix \mathbf{F}_Q , assuming a sufficiently long cyclic prefix $\mu \geq L$, we obtain a simple input/output relationship in the frequency-domain :

$$\bar{\mathbf{y}}_{m_r}[n] = \sum_{m_l=1}^{M_t} \tilde{\mathbf{H}}_{m_r, m_l} \cdot \bar{\mathbf{x}}_{m_l}[n] + \bar{\mathbf{e}}_{m_r}[n] \quad (41)$$

where $\bar{\mathbf{e}}_{m_r}[n]$ is the frequency-domain received noise block sequence and $\tilde{\mathbf{H}}_{m_r, m_l}$ the $Q \times Q$ diagonal frequency-domain channel matrix having the frequency-domain channel response \tilde{h}_{m_r, m_l} as its main diagonal. Exploiting the structure of the ST code design in Equation 40, we can write for two consecutive chip blocks $\bar{\mathbf{y}}_{m_r}[2n]$ and $\bar{\mathbf{y}}_{m_r}[2n+1]$ the frequency-domain input/output relationship of Equation 41, resulting in Equation 42. Stacking the contributions of the M_r receive antennas

$$\bar{\mathbf{y}}[n] = \begin{bmatrix} \bar{\mathbf{y}}_1^T[n] & \dots & \bar{\mathbf{y}}_{M_r}^T[n] \end{bmatrix}^T,$$

we obtain the following per receive antenna

$$\underbrace{\begin{bmatrix} \bar{\mathbf{y}}_{m_r}[2n] \\ \bar{\mathbf{y}}_{m_r}^*[2n+1] \end{bmatrix}}_{\bar{\mathbf{y}}_{m_r}[n]} = \underbrace{\begin{bmatrix} \tilde{\mathbf{H}}_{m_r, 1} & \tilde{\mathbf{H}}_{m_r, 2} \\ \tilde{\mathbf{H}}_{m_r, 2}^* & -\tilde{\mathbf{H}}_{m_r, 1}^* \end{bmatrix}}_{\tilde{\mathbf{H}}_{m_r}} \cdot \underbrace{\begin{bmatrix} \tilde{\mathbf{x}}_1[n] \\ \tilde{\mathbf{x}}_2[n] \end{bmatrix}}_{\tilde{\mathbf{x}}[n]} + \underbrace{\begin{bmatrix} \bar{\mathbf{e}}_{m_r}[2n] \\ \bar{\mathbf{e}}_{m_r}^*[2n+1] \end{bmatrix}}_{\bar{\mathbf{e}}_{m_r}[n]} \quad (42)$$

frequency-domain data model :

$$\bar{\mathbf{y}}[n] = \tilde{\mathbf{H}} \cdot \tilde{\mathbf{x}}[n] + \tilde{\mathbf{e}}[n] \quad (43)$$

where the per receive antenna channel matrix $\tilde{\mathbf{H}}$ and the per receive antenna noise block $\tilde{\mathbf{e}}[n]$ are similarly defined as the per receive antenna output block $\bar{\mathbf{y}}[n]$. Defining the receive permutation matrix \mathbf{P}_r respectively the transmit permutation matrix \mathbf{P}_t as follows :

$$\hat{y}[n] := P_r \cdot \tilde{y}[n] \quad \tilde{x}[n] := P_t \cdot x'[n] \quad (44)$$

5 where P_r permutes a per receive antenna ordering into a per-tone ordering and where P_t conversely permutes a per-tone ordering into a per transmit antenna ordering, we obtain the following per-tone data model :

$$\hat{y}[n] = H \cdot x'[n] + \hat{e}[n] \quad (45)$$

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[0101] In this Equation,

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$$\hat{y}[n] = [\hat{y}_1^T[n] \quad \dots \quad \hat{y}_Q^T[n]]^T$$

is the per-tone output block, $x'[n]$ the per-tone input block and $\hat{e}[n]$ the per-tone noise block similarly defined as $\hat{e}[n]$. The per-tone channel matrix H is a block diagonal matrix, given by :

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$$H := P_r \cdot \tilde{H} \cdot P_t = \begin{bmatrix} H_1 & & \\ & \ddots & \\ & & H_Q \end{bmatrix} \quad (46)$$

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B.1.c Data model for burst processing.

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[0102] Assuming a burst length of $M_t B \cdot I$ symbols for each user, we can stack $I \cdot N$ consecutive chip blocks $\tilde{y}[n]$, defined in Equation 43, into

$$\tilde{Y} := [\tilde{y}[0] \quad \dots \quad \tilde{y}[IN-1]],$$

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leading to the following per receive antenna data model for burst processing :

$$\tilde{Y} = \tilde{H} \cdot \tilde{X} + \tilde{E} \quad (47)$$

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where the input matrix \tilde{X} and the noise matrix \tilde{E} are similarly defined as the output matrix \tilde{Y} . By having a look at the definition of \tilde{X} in Equation 42 and by inspecting Equation 39, we can write \tilde{X} as follows :

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$$\tilde{X} = \tilde{S}_d \cdot C_d + \tilde{S}_p \cdot C_p \quad (48)$$

where the multi-user total data symbol matrix

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$$\tilde{S}_d := [\tilde{S}_1 \quad \dots \quad \tilde{S}_U]$$

stacks the total data symbol matrices of the different active users and the u -th user's total data symbol matrix

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$$\tilde{S}_u := [\tilde{S}_1^{uT} \quad \tilde{S}_2^{uT}]^T$$

stacks the u -th user's data symbol matrices for the different transmit antennas. The u -th user's data symbol matrix for the m_t -th transmit antenna

$$\tilde{\mathbf{S}}_{m_t}^u := [\tilde{s}_{m_t}^u[0] \quad \dots \quad \tilde{s}_{m_t}^u[I-1]]$$

stacks I consecutive precoded symbol blocks for the u -th user and the m_t -th transmit antenna. The total pilot symbol matrix $\tilde{\mathbf{S}}$, and the pilot symbol matrix for the m_t -th transmit antenna $\tilde{\mathbf{S}}_{m_t}^u$ are similarly defined as $\tilde{\mathbf{s}}$, respectively $\tilde{s}_{m_t}^u$. The multi-user code matrix

$$\mathbf{C}_d := [\mathbf{C}_1^T \quad \dots \quad \mathbf{C}_U^T]^T$$

stacks the code matrices of the different active users. The u -th user's code matrix stacks the u -th user's code vectors at I consecutive symbol instants :

$$\mathbf{C}_u := \begin{bmatrix} \mathbf{c}_u[0] & & \\ & \ddots & \\ & & \mathbf{c}_u[I-1] \end{bmatrix} \quad (49)$$

where

$$\mathbf{c}_u[i] = [c_u[iN] \quad \dots \quad c_u[(i+1)N-1]]$$

is the u -th user's code vector used to spread the precoded symbol blocks

$$\{\tilde{s}_{m_t}^u[i]\}_{m_t=1}^{M_t}$$

[0103] Similarly to the per receive antenna data model for burst processing in Equation 47, we can stack $I \cdot N$ consecutive chip blocks $\hat{\mathbf{y}}[n]$ leading to the following per-tone data model for burst processing :

$$\hat{\mathbf{y}} = \mathbf{H}' \cdot \mathbf{X}' + \hat{\mathbf{E}} \quad (50)$$

Using Equation 44 and 48 we can express \mathbf{X}' as follows :

$$\mathbf{X}' = \hat{\mathbf{S}}_d \cdot \mathbf{C}_d + \hat{\mathbf{S}}_p \cdot \mathbf{C}_p \quad (51)$$

where

$$\hat{\mathbf{S}}_d := \mathbf{P}_t^T \cdot \tilde{\mathbf{S}}_d$$

and

$$\hat{\mathbf{S}}_p := \mathbf{P}_t^T \cdot \tilde{\mathbf{S}}_p$$

are the per-tone permuted versions of \hat{s}_r , respectively \hat{s}_r .

B.2. Per-tone burst chip equalizers.

[0104] Inspired by our related work for the DS-CDMA downlink, we can now deal with the design of the chip equalizers. Starting from Equation 50 and assuming that the channel matrix \mathbf{H}' has full column rank and the input matrix \mathbf{X} has full row rank, it is possible to find a Zero-Forcing (ZF) chip equalizer matrix \mathbf{G} , for which :

$$\mathbf{G} \cdot \hat{\mathbf{y}} \cdot \mathbf{X}' = 0 \quad (52)$$

provided there is no noise present in the output matrix $\hat{\mathbf{y}}$. Since the channel matrix \mathbf{H}' has a block diagonal structure, as shown in Equation 46, the equalizer matrix \mathbf{G} suffices to have a block diagonal structure as well :

$$\mathbf{G} := \begin{bmatrix} \mathbf{G}_1 & & \\ & \ddots & \\ & & \mathbf{G}_Q \end{bmatrix} \quad (53)$$

acting on a per-tone basis. For this reason, the ZF problem of Equation 52 decouples into Q parallel and independent ZF problems, one for each tone. Using Equation 51, we can rewrite the original ZF problem of Equation 52 as follows :

$$\mathbf{G} \cdot \hat{\mathbf{y}} - \hat{\mathbf{s}}_d \cdot \mathbf{C}_d - \hat{\mathbf{s}}_p \cdot \mathbf{C}_p = 0 \quad (54)$$

which is a ZF problem in both the equalizer matrix \mathbf{G} and the multi-user total data symbol matrix $\hat{\mathbf{s}}_r$.

B.2.a Training-based burst chip equalizer.

[0105] The training-based chip equalizer determines its equalizer coefficients from the per-tone output matrix \mathbf{Y} based on the knowledge of the pilot code matrix \mathbf{C}_p and the total pilot symbol matrix $\hat{\mathbf{s}}_r$. By despreading Equation 54 with the pilot code matrix \mathbf{C}_p^H , we obtain :

$$\mathbf{G} \cdot \hat{\mathbf{y}} \cdot \mathbf{C}_p^H - \hat{\mathbf{s}}_p = 0 \quad (55)$$

because of the orthogonality between the multi-user code matrix \mathbf{C}_d and the pilot code matrix \mathbf{C}_p . In case noise is present in the output matrix $\hat{\mathbf{y}}$, we have to solve the corresponding Least Squares (LS) minimisation problem :

$$\hat{\mathbf{G}} = \arg \min_{\mathbf{G}} \left\| \mathbf{G} \cdot \hat{\mathbf{y}} \cdot \mathbf{C}_p^H - \hat{\mathbf{s}}_p \right\|_F^2 \quad (56)$$

which can be interpreted as follows. The equalized output matrix $\mathbf{G} \cdot \hat{\mathbf{y}}$ is despread with the pilot code matrix \mathbf{C}_p . The equalized output matrix after despreading $\mathbf{G} \cdot \hat{\mathbf{y}} \cdot \mathbf{C}_p^H$ should then be as close as possible to the known total pilot symbol matrix $\hat{\mathbf{s}}_p$ in a Least Squares sense.

B.2.b Semi-blind burst chip equalizer.

[0106] The semi-blind chip equalizer determines its equalizer coefficients from the per-tone output matrix \mathbf{Y} based on the knowledge of the multi-user code matrix \mathbf{C}_d , the pilot code matrix \mathbf{C}_p and the total pilot symbol matrix $\hat{\mathbf{s}}_r$. By solving Equation 54 first for $\hat{\mathbf{s}}_r$, assuming \mathbf{G} to be known and fixed, gives

$$\hat{\mathbf{s}}_d = \mathbf{G} \cdot \hat{\mathbf{y}} \cdot \mathbf{C}_d^H.$$

Substituting $\hat{\mathbf{s}}_p$ into Equation 54 leads to a semi-blind ZF problem in \mathbf{G} only :

$$\mathbf{G} \cdot \hat{\mathbf{Y}} \cdot (\mathbf{I}_{IN} - \mathbf{C}_d^H \cdot \mathbf{C}_d) - \hat{\mathbf{s}}_p \cdot \mathbf{C}_p = 0 \quad (57)$$

In case noise is present in the output matrix $\hat{\mathbf{Y}}$, we have to solve the corresponding LS minimisation problem :

$$\hat{\mathbf{G}} = \arg \min_{\mathbf{G}} \left\| \mathbf{G} \cdot \hat{\mathbf{Y}} \cdot (\mathbf{I}_{IN} - \mathbf{C}_d^H \cdot \mathbf{C}_d) - \hat{\mathbf{s}}_p \cdot \mathbf{C}_p \right\|_F^2 \quad (58)$$

which can be interpreted as follows. The equalized output matrix $\mathbf{G} \cdot \hat{\mathbf{Y}}$ is projected on the orthogonal complement of the subspace spanned by the multi-user code matrix \mathbf{C}_d . The equalized output matrix after projecting

$$\mathbf{G} \cdot \hat{\mathbf{Y}} (\mathbf{I}_{IN} - \mathbf{C}_d^H \cdot \mathbf{C}_d)$$

should then be as close as possible to the known total pilot chip matrix

$$\hat{\mathbf{s}}_p \cdot \mathbf{C}_p$$

in a Least Squares sense.

B.2.c User-specific detection.

[0107] The obtained per-tone pilot-trained chip equalizer matrix $\hat{\mathbf{G}}$, whether training-based or semi-blind, may subsequently be used to extract the desired user's total data symbol matrix :

$$\hat{\mathbf{S}}_u = \hat{\Theta}^H \cdot \mathbf{P}_t \cdot \hat{\mathbf{G}} \cdot \hat{\mathbf{Y}} \cdot \mathbf{C}_u^H \quad (59)$$

where the equalized output matrix $\mathbf{G} \cdot \hat{\mathbf{Y}}$ is first despread with the desired user's code matrix \mathbf{C}_u . Next the transmit permutation matrix \mathbf{P}_t permutes the per-tone ordering of the despread equalized output matrix into a per transmit antenna ordering. Finally, the total precoding matrix $\hat{\Theta}$ linearly decodes the permuted version of the despread equalized output matrix, where $\hat{\Theta}$ is a $M_t \cdot Q \times M_t \cdot B$ block diagonal matrix with the precoding matrix Θ on its main diagonal.

[0108] We can conclude that the per-tone pilot-trained chip equalizer with the training-based cost function is a promising technique for downlink reception in future broadband wireless communication systems based on a space-time coded MC-DS-CDMA transmission scheme.

C. Embodiment

C.1. SCBT-DS-CDMA downlink system model.

[0109] Let us consider the downlink of a single-cell space-time block coded SCBT-DS-CDMA system with U active mobile stations. The base-station is equipped with M_t transmit (TX) antennas whereas the mobile station of interest is equipped with M_r receive (RX) antennas.

C.1.a. Transmitter model for the base station.

[0110] For simplicity reasons, we will assume in the following that the base station has only $M_t=2$ transmit antennas. Note however that the proposed techniques can be extended to the more general case of $M_t > 2$ transmit antennas when resorting to the generalized orthogonal designs. As shown in Figure 10, each user's data symbol sequence $s^u[i]$ (similar for the pilot symbol sequence $s^p[i]$) is demultiplexed into M_t parallel lower rate sequences

$$\left\{ s_{m_i}^u[i] := s^u[iM_t + m_i - 1] \right\}_{m_i=1}^{M_t},$$

5 where M_t is the number of transmit antennas. Each of the u -th user's symbol sequences

$$\left\{ s_{m_i}^u[i] \right\}_{m_i=1}^{M_t}$$

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is serial-to-parallel converted into blocks of B symbols, leading to the symbol block sequences

$$\left\{ \mathbf{s}_{m_i}^u[i] := \left[s_{m_i}^u[iB], \dots, s_{m_i}^u[(i+1)B-1] \right]^T \right\}_{m_i=1}^{M_t}$$

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that are subsequently spread by a factor N with the same user composite code sequence $c_u[n]$ which is the multiplication of the user specific orthogonal Walsh-Hadamard spreading code and the base station specific scrambling code. For each of the M_t parallel streams, the different user chip block sequences and the pilot chip block sequence are added, resulting into the m_i -th multi-user chip block sequence :

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$$\mathbf{x}_{m_i}[n] = \sum_{u=1}^U s_{m_i}^u[i] c_u[n] + s_{m_i}^p[i] c_p[n], \quad i = \left\lfloor \frac{n}{N} \right\rfloor \quad (60)$$

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Let us also define the u -th user's total symbol block sequence

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$$\mathbf{s}^u[i] := \left[s_1^u[i]^T, s_2^u[i]^T \right]^T$$

and the total multi-user chip block sequence

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$$\mathbf{x}[n] := \left[\mathbf{x}_1^T[n], \mathbf{x}_2^T[n] \right]^T.$$

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The block Space-Time (ST) encoder operates in the time-domain (TD) at the chip block level rather than at the symbol block level and takes the two multi-user chip blocks

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$$\left\{ \mathbf{x}_{m_i}[n] \right\}_{m_i=1}^2$$

to output the following $2B \times 2$ matrix of ST coded multi-user chip blocks :

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$$\begin{bmatrix} \bar{\mathbf{x}}_1[2n] & \bar{\mathbf{x}}_1[2n+1] \\ \bar{\mathbf{x}}_2[2n] & \bar{\mathbf{x}}_2[2n+1] \end{bmatrix} = \begin{bmatrix} \mathbf{x}_1[n] & -\mathbf{P}_B^{(0)} \cdot \mathbf{x}_2^*[n] \\ \mathbf{x}_2[n] & \mathbf{P}_B^{(0)} \cdot \mathbf{x}_1^*[n] \end{bmatrix} \quad (61)$$

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where $\mathbf{P}^{(j)}$ is a $J \times J$ permutation matrix implementing a reversed cyclic shift over j positions. At each time interval n , the ST coded multi-user chip blocks $\bar{\mathbf{x}}_1[n]$ and $\bar{\mathbf{x}}_2[n]$ are forwarded to the first respectively the second transmit antenna. From Equation 61, we can easily verify that the transmitted multi-user chip block at time instant $2n+1$ from one antenna is the time-reversed conjugate of the transmitted multi-user chip block at time instant $2n$ from the other antenna (with a possible sign change). The $K \times B$ transmit matrix \mathbf{T}_{zp} , with $K=B+\mu$, pads a zero postfix of length μ to each block of

the ST coded multi-user chip block sequence $\bar{\mathbf{x}}_{m_k}[n]$ leading to the transmitted multi-user chip block sequence $\mathbf{u}_{m_k}[n] = \mathbf{T}_{zp} \cdot \bar{\mathbf{x}}_{m_k}[n]$. Finally, the transmitted multi-user chip block sequence $\mathbf{u}_{m_k}[n]$ is parallel-to-serial converted into the transmitted multi-user chip sequence

$$\left[u_{m_k}[nK], \dots, u_{m_k}[(n+1)K-1] \right]^T := \mathbf{u}_{m_k}[n].$$

C.1.b Receiver model for the mobile station.

[0111] It is assumed that the mobile station of interest is equipped with M_r receive antennas and has acquired perfect synchronisation. At each receive antenna in Figure 11, the TD received chip sequence $\mathbf{v}_{m_k}[n]$ is serial-to-parallel converted into blocks of K chips, resulting into the TD received chip block sequence

$$\mathbf{v}_{m_r}[n] := \left[v_{m_r}[nK], \dots, v_{m_r}[(n+1)K-1] \right]^T.$$

The $K \times K$ receive matrix $\mathbf{R} := \mathbf{I}_K$ completely preserves each block of the TD received chip block sequence $\mathbf{v}_{m_k}[n]$ leading to the TD received ST coded chip block sequence $\bar{\mathbf{y}}_{m_k}[n] = \mathbf{R} \cdot \mathbf{v}_{m_k}[n]$. Assuming a sufficiently long zero postfix $\mu \geq L$ (L is the maximum channel order), we obtain a simple input/output relationship in the time-domain :

$$\bar{\mathbf{y}}_{m_r}[n] = \sum_{m_i=1}^{M_r} \hat{\mathbf{H}}_{m_r, m_i} \cdot \mathbf{T}_{zp} \cdot \bar{\mathbf{x}}_{m_i}[n] + \bar{\mathbf{e}}_{m_r}[n] \quad (62)$$

where $\bar{\mathbf{e}}_{m_k}[n]$ is the TD received noise block sequence and $\hat{\mathbf{H}}_{m_r, m_i}$ is a $K \times K$ circulant channel matrix. We consider two consecutive chip blocks and define $\mathbf{y}_{m_r,1}[n] := \bar{\mathbf{y}}_{m_r}[2n]$ and $\mathbf{y}_{m_r,2}[n] := \mathbf{P}^{(B)} \cdot \bar{\mathbf{y}}_{m_r}[2n+1]$. Transforming $\mathbf{y}_{m_r,1}[n]$ and $\mathbf{y}_{m_r,2}[n]$ to the frequency-domain (FD) employing the $K \times K$ FFT matrix \mathbf{F}_K leads to the input/output relationship of Equation 63 on the top of the next page, where

$$\tilde{\mathbf{H}}_{m_r, m_i} = \mathbf{F}_K \cdot \hat{\mathbf{H}}_{m_r, m_i} \cdot \mathbf{F}_K^H$$

is the $K \times K$ diagonal FD channel matrix having the FD channel response \tilde{h}_{m_r, m_i} as its main diagonal. Note from Equation 63 that

$$\tilde{\mathbf{x}}[n] := \mathbf{F}_K \cdot \mathbf{T}_{zp} \cdot \mathbf{x}[n]$$

where both the compound FFT matrix $\mathbf{F}_K := \text{diag} \{ \mathbf{F}_K, \mathbf{F}_K \}$ and the compound transmit matrix $\mathbf{T}_{zp} := \text{diag} \{ \mathbf{T}_{zp}, \mathbf{T}_{zp} \}$ are block diagonal. Stacking the contributions of the M_r receive antennas

$$\bar{\mathbf{y}}[n] = \left[\bar{\mathbf{y}}_1^T[n], \dots, \bar{\mathbf{y}}_{M_r}^T[n] \right]^T,$$

$$\underbrace{\begin{bmatrix} \mathbf{F}_K \cdot \bar{\mathbf{y}}_{m_r}[2n] \\ \mathbf{F}_K \cdot \mathbf{P}_K^{(B)} \cdot \bar{\mathbf{y}}_{m_r}[2n+1] \end{bmatrix}}_{\bar{\mathbf{y}}_{m_r}[n]} = \underbrace{\begin{bmatrix} \tilde{\mathbf{H}}_{m_r,1} & \tilde{\mathbf{H}}_{m_r,2} \\ \tilde{\mathbf{H}}_{m_r,2}^* & -\tilde{\mathbf{H}}_{m_r,1}^* \end{bmatrix}}_{\tilde{\mathbf{H}}_{m_r}} \cdot \underbrace{\begin{bmatrix} \mathbf{F}_K \cdot \mathbf{T}_{zp} \cdot \mathbf{x}_1[n] \\ \mathbf{F}_K \cdot \mathbf{T}_{zp} \cdot \mathbf{x}_2[n] \end{bmatrix}}_{\tilde{\mathbf{x}}[n]} + \underbrace{\begin{bmatrix} \mathbf{F}_K \cdot \bar{\mathbf{e}}_{m_r}[2n] \\ \mathbf{F}_K \cdot \mathbf{P}_K^{(B)} \cdot \bar{\mathbf{e}}_{m_r}[2n+1] \end{bmatrix}}_{\bar{\mathbf{e}}_{m_r}[n]} \quad (63)$$

we obtain the following per-RX-antenna FD data model :

$$\tilde{\mathbf{y}}[n] = \tilde{\mathbf{H}} \cdot \tilde{\mathbf{x}}[n] + \tilde{\mathbf{e}}[n] \quad (64)$$

5 where the per-RX-antenna channel matrix $\tilde{\mathbf{h}}$ and the per-RX-antenna noise block $\tilde{\mathbf{e}}[n]$ are similarly defined as the per-RX-antenna output block $\tilde{\mathbf{y}}[n]$. Defining the per-tone input block $\mathbf{x}'[n]$ and the per-tone output block $\mathbf{y}'[n]$ as :

$$\begin{aligned} \mathbf{x}'[n] &:= \mathbf{P}_t \cdot \tilde{\mathbf{x}}[n] = [\mathbf{x}_1[n], \dots, \mathbf{x}_K[n]]^T \\ \mathbf{y}'[n] &:= \mathbf{P}_r \cdot \tilde{\mathbf{y}}[n] = [\mathbf{y}_1[n], \dots, \mathbf{y}_K[n]]^T \end{aligned} \quad (65)$$

15 where \mathbf{P}_t permutes a per-TX-antenna ordering into a per-tone ordering and where \mathbf{P}_r permutes a per-RX-antenna ordering into a per-tone ordering, we obtain the following per-tone data model :

$$\mathbf{y}'[n] = \mathbf{H} \cdot \mathbf{x}'[n] + \mathbf{e}[n] \quad (66)$$

20 where $\mathbf{e}[n]$ is the per-tone noise block similarly defined as $\mathbf{y}'[n]$. The per-tone channel matrix \mathbf{H} is a block diagonal matrix, given by :

$$\mathbf{H} := \mathbf{P}_r \cdot \tilde{\mathbf{H}} \cdot \mathbf{P}_t^T = \text{diag}\{\mathbf{H}_1, \dots, \mathbf{H}_K\} \quad (67)$$

C.1.c Data model for burst processing.

30 **[0112]** Assuming a burst length of $M_t \cdot B \cdot I$ symbols for each user, we can stack $I \cdot N$ consecutive chip blocks $\tilde{\mathbf{y}}[n]$, defined in Equation 64, into

$$\tilde{\mathbf{Y}} := [\tilde{\mathbf{y}}[0], \dots, \tilde{\mathbf{y}}[IN-1]] ,$$

35 leading to the following per-RX-antenna data model for burst processing :

$$\tilde{\mathbf{Y}} = \tilde{\mathbf{H}} \cdot \tilde{\mathbf{X}} + \tilde{\mathbf{E}} \quad (68)$$

40 where the input matrix $\tilde{\mathbf{x}}$ and the noise matrix $\tilde{\mathbf{e}}$ are similarly defined as the output matrix $\tilde{\mathbf{y}}$. Note that

$$\tilde{\mathbf{X}} = \mathbf{F}_K \cdot \mathbf{T}_{zp} \cdot \mathbf{X} \quad (69)$$

45 where \mathbf{X} stacks $I \cdot N$ consecutive total multi-user chip blocks $\mathbf{x}[n]$. Moreover, by inspecting Equation 60, we can write \mathbf{X} as :

$$\mathbf{X} = \mathbf{S}_d \cdot \mathbf{C}_d + \mathbf{S}_p \cdot \mathbf{C}_p \quad (70)$$

50 where the multi-user total data symbol matrix $\mathbf{S}_d := [\mathbf{S}_1, \dots, \mathbf{S}_U]$ stacks the total data symbol matrices of the different active users and the u -th user's total data symbol matrix

$$\mathbf{S}_u := [s^u[0], \dots, s^u[I-1]]$$

stacks I consecutive total symbol blocks for the u -th user. The total pilot symbol matrix \mathbf{S}_p is similarly defined as \mathbf{S}_u . The multi-user code matrix

$$\mathbf{C}_d := [\mathbf{C}_1^T, \dots, \mathbf{C}_U^T]^T$$

stacks the code matrices of the different active users. The u -th user's code matrix stacks the u -th user's composite code vectors at I consecutive symbol block instants :

$$\mathbf{C}_u := \text{diag} \{ \mathbf{c}_u^T[0], \dots, \mathbf{c}_u^T[I-1] \} \quad (71)$$

where

$$\mathbf{c}_u[i] := [c_u[iN], \dots, c_u[(i+1)N-1]]^T$$

is the u -th user's composite code vector used to spread the total symbol block $\mathbf{s}^u[i]$. The pilot code matrix \mathbf{C}_p is similarly defined as \mathbf{C}_u .

[0113] Similarly to the per-RX-antenna data model for burst processing in Equation 68, we can stack $I \cdot N$ consecutive chip blocks $\hat{\mathbf{y}}[n]$ leading to the following per-tone data model for burst processing :

$$\hat{\mathbf{y}} = \mathbf{H}' \cdot \mathbf{X}' + \hat{\mathbf{E}} \quad (72)$$

[0114] Using Equation 65, 69 and 70, we can express \mathbf{X}' as :

$$\mathbf{X}' = \hat{\mathbf{S}}_d \cdot \mathbf{C}_d + \hat{\mathbf{S}}_p \cdot \mathbf{C}_p \quad (73)$$

where

$$\hat{\mathbf{S}}_d := \mathbf{P}_t \cdot \tilde{\mathbf{S}}_d$$

and

$$\hat{\mathbf{S}}_p := \mathbf{P}_t \cdot \tilde{\mathbf{S}}_p$$

are the per-tone permuted versions of

$$\tilde{\mathbf{S}}_d := \mathbf{F}_K \cdot \mathbf{T}_{zp} \cdot \mathbf{S}_d$$

respectively

$$\tilde{\mathbf{S}}_p := \mathbf{F}_K \cdot \mathbf{T}_{zp} \cdot \mathbf{S}_p$$

C.2 Burst frequency-domain chip equalization

[0115] Armed with a suitable data model for burst processing, we can now proceed with the design of different Least

Squares (LS) type of burst FD chip equalizers that processes a burst of M_t/I data symbol blocks at once. Note that Recursive Least Squares (RLS) type of adaptive FD chip equalizers that process the data on a symbol block by symbol block basis can be easily derived from their corresponding LS burst version. Starting from Equation 72 and assuming the channel matrix H' to have full column rank and the input matrix X' to have full row rank, it is always possible to find a Zero-Forcing (ZF) chip equalizer matrix G' , for which $G' \cdot \hat{Y} - X = 0$, provided there is no noise present in the output matrix \hat{y} . In the presence of noise, we have to solve the corresponding Least Squares (LS) minimization problem, which we denote for convenience as :

$$G' \cdot \hat{Y} - X \stackrel{LS}{=} 0 \quad (74)$$

[0116] Since the channel matrix H' has a block diagonal structure, as shown in Equation 67, the equalizer matrix G' suffices to have a block diagonal structure as well :

$$G' := \text{diag} \{ G'_1, \dots, G'_K \} \quad (75)$$

acting on a per-tone basis at the chip block level (see also Figure 6). For this reason, the LS problem of Equation 74 decouples into K parallel and independent LS problems, one for each tone. Using Equation 73, we can rewrite the original LS problem of Equation 74 as :

$$G' \cdot \hat{Y} - \hat{S}_d \cdot C_d - \hat{S}_p \cdot C_p \stackrel{LS}{=} 0 \quad (76)$$

which is a LS problem in both the equalizer matrix G' and the multi-user total data symbol matrix \hat{S}_d . Starting from Equation 76, we will design in the following two different FD methods for direct chip equalizer estimation that differ in the amount of a-priori information they exploit to determine the equalizer coefficients. The first method, coined CDMP-trained, only exploits the presence of a Code Division Multiplexed Pilot (CDMP). The second method, coined semi-blind CDMP-trained, additionally exploits knowledge of the multi-user code correlation matrix.

C.2.a CDMP-trained chip equalizer

[0117] The CDMP-trained chip equalizer estimator directly determines the equalizer coefficients from the per-tone output matrix \hat{y} based on the knowledge of the pilot code matrix C_p and the total pilot symbol matrix \hat{S}_p . By despreading Equation 76 with the pilot code matrix C_p , we obtain :

$$G' \cdot \hat{Y} \cdot C_p^H - \hat{S}_p \stackrel{LS}{=} 0 \quad (77)$$

because $C_d C_p^H = 0$ due to the orthogonality of the user and pilot composite code sequences at each symbol instant. Equation 77 can be interpreted as follows. The equalized per-tone output matrix after despreading $G' \cdot \hat{Y} \cdot C_p^H$ should be as close as possible in a Least Squares sense to the per-tone pilot symbol matrix \hat{S}_p .

C.2.b Semi-blind CDMP-trained chip equalizer.

[0118] The semi-blind CDMP-trained chip equalizer estimator directly determines the equalizer coefficients from the per-tone output matrix \hat{y} based on the knowledge of the multi-user code matrix C_d , the pilot code matrix C_p and the per-tone pilot symbol matrix \hat{S}_p . By despreading Equation 76 with the multi-user code matrix C_d and by assuming the per-tone equalizer matrix G' to be known and fixed, we obtain an LS estimate of the per-tone multi-user data symbol matrix

$$\hat{\hat{S}}_d = G' \cdot \hat{Y} \cdot C_d^H$$

Substituting \hat{s}_p into the original LS problem of Equation 76 leads to a modified LS problem in the per-tone equalizer matrix \mathbf{G}' only :

$$\mathbf{G}' \cdot \hat{\mathbf{Y}} \cdot (\mathbf{I}_{IN} - \mathbf{C}_d^H \cdot \mathbf{C}_d) - \hat{\mathbf{s}}_p \cdot \mathbf{C}_p^H = 0 \quad (78)$$

which can be interpreted as follows. The equalized per-tone output matrix $\mathbf{G}' \cdot \hat{\mathbf{Y}}$ is first projected on the orthogonal complement of the subspace spanned by the multi-user code matrix \mathbf{C}_d , employing the projection matrix $\mathbf{I}_{IN} - \mathbf{C}_d^H \cdot \mathbf{C}_d$. The resulting equalized per-tone output matrix after projecting should then be as close as possible in Least Squares sense to the per-tone pilot chip matrix $\hat{\mathbf{s}}_p \cdot \mathbf{C}_p^H$.

C.2.c User-specific detection

[0119] As shown in Figure 6, the obtained per-tone chip equalizer matrix $\hat{\mathbf{G}}'$, whether CDMF-trained or semi-blind CDMF-trained, may subsequently be used to extract the desired user's total data symbol matrix :

$$\hat{\mathbf{S}}_u = \mathbf{T}_{zp}^T \cdot \mathbf{F}_K^H \cdot \mathbf{P}_i^T \cdot \hat{\mathbf{G}}' \cdot \hat{\mathbf{Y}} \cdot \mathbf{C}_u^H \quad (79)$$

where the estimated FD input matrix

$$\hat{\mathbf{X}} = \mathbf{P}_i^T \cdot \hat{\mathbf{G}}' \cdot \hat{\mathbf{Y}}$$

is transformed to the TD by the compound IFFT matrix \mathbf{F}_K^H and has its zero postfix removed by the transpose of the ZP transmit matrix \mathbf{T}_{zp} . The resulting estimate of the TD input matrix \mathbf{X} is finally despread with the desired user's code matrix \mathbf{C}_u to obtain an estimate of the desired user's total data symbol matrix \mathbf{S}_u .

C.3 Conclusion

[0120] In this section, we have combined Single-Carrier Block Transmission (SCBT) DS-CDMA with Time Reversal (TR) Space-Time Block Coding (STBC) for downlink multi-user MIMO communications. Moreover, we have developed two new direct equalizer estimation methods that act on a per-tone basis in the Frequency-Domain (FD) exploiting a Code Division Multiplexed Pilot (CDMP). The regular CDMP-trained method only exploits the presence of a CDMP whereas the semi-blind CDMP-trained method additionally capitalizes on the unused spreading codes in a practical CDMA system. Both the regular and the semi-blind CDMP-trained method come close to extracting the full diversity of order $M_t M_r (L+1)$ independently of the system load. The semi-blind CDMP-trained method outperforms the regular CDMP-trained method for low to medium system load and proves its usefulness especially for small burst lengths.

D. Embodiment

D.1 SCBT-DS-CDMA downlink system model

[0121] Let us consider the downlink of a single-cell SCBT-DS-CDMA system with U active mobile stations. The base station has a single transmit antenna whereas the mobile station of interest has possibly multiple receive antennas.

D.1.a Transmitter model for the base station

[0122] As shown in Figure 12, the base station transforms U user data symbol sequences

$$\{s^u[i]\}_{u=1}^U$$

and a pilot symbol sequence $s^p[i]$ into a single transmitted chip sequence $u[n]$. Each user's data symbol sequence s^u

[i] (pilot symbol sequence $s^p[i]$) is serial-to-parallel converted into blocks of B symbols, leading to the data symbol block sequence

$$s^u[i] := [s^u[iB], \dots, s^u[(i+1)B-1]]^T$$

(pilot symbol block sequence $s^p[i]$). The u -th user's data symbol block sequence $s^u[i]$ (pilot symbol block sequence $s^p[i]$) is subsequently spread by a factor N with the user composite code sequence $c_u[n]$ (pilot composite code sequence $c_p[n]$) which is the multiplication of a user specific (pilot specific) orthogonal Walsh-Hadamard spreading code and a base station specific scrambling code. The different user chip block sequences and the pilot chip block sequence are added, resulting into the multi-user chip block sequence :

$$x[n] = \sum_{u=1}^U s^u[i]c_u[n] + s^p[i]c_p[n], \quad i = \left\lfloor \frac{n}{N} \right\rfloor \quad (80)$$

The $B \times 1$ multi-user chip block sequence $x[n]$ is transformed into the $K \times 1$ transmitted chip block sequence :

$$u[n] = T_1 \cdot x[n] + T_2 \cdot b \quad (81)$$

with $K=B+\mu$, where T_1 is the $K \times B$ zero padding (ZP) transmit matrix

$$T_1 := \begin{bmatrix} I_B & 0_{B \times \mu} \end{bmatrix}^T$$

and T_2 is the $K \times \mu$ Known Symbol Padding (KSP) transmit matrix

$$T_2 := \begin{bmatrix} 0_{\mu \times B} & I_\mu \end{bmatrix}^T.$$

Note that this operation adds a $\mu \times 1$ known symbol postfix b to each block of the multi-user chip block sequence $x[n]$. Finally, the transmitted chip block sequence $u[n]$ is parallel-to-serial converted into the corresponding transmitted chip sequence

$$[u[nK], \dots, u[(n+1)K-1]]^T := u[n].$$

45 **D.1.b Receiver model for the mobile station**

[0123] It is assumed that the mobile station of interest is equipped with M_r receive antennas and has acquired perfect synchronisation. As shown in Figure 13, the mobile station of interest transforms M_r received chip sequences

$$\{v_{m_r}[n]\}_{m_r=1}^{M_r}$$

into an estimate of the desired user's data symbol sequence $s^u[i]$ (we assume the u -th user to be the desired user). At each receive antenna, the received chip sequence $v_{m_r}[n]$ is serial-to-parallel converted into blocks of K chips, resulting into the received chip block sequence

$$\mathbf{v}_{m_r}[n] := \left[v_{m_r}[nK], \dots, v_{m_r}[(n+1)K-1] \right]^T.$$

The $K \times K$ receive matrix $\mathbf{R} := \mathbf{I}_K$ completely preserves each block of the received chip block sequence $\mathbf{v}_{m_r}[n]$ leading to the received multi-user chip block sequence $\mathbf{y}_{m_r}[n] := \mathbf{R} \cdot \mathbf{v}_{m_r}[n]$. Assuming a sufficiently long known symbol postfix $\mu \geq L$ (L is the maximum channel order of all channels), we obtain a simple input/output relationship in the time-domain :

$$\mathbf{y}_{m_r}[n] = \tilde{\mathbf{H}}_{m_r} \cdot (\mathbf{T}_1 \cdot \mathbf{x}[n] + \mathbf{T}_2 \cdot \mathbf{b}) + \mathbf{z}_{m_r}[n] \quad (82)$$

where $\mathbf{z}_{m_r}[n]$ is the received noise block sequence and $\tilde{\mathbf{H}}_{m_r}$ is a $K \times K$ circulant channel matrix describing the multi-path propagation from the base station's transmit antenna to the mobile station's m_r -th receive antenna. Note that there is no Inter Block Interference (IBI) because \mathbf{b} acts as a cyclic prefix for each transmitted chip block $\mathbf{u}[n]$. Transforming the time-domain (TD) received chip block sequence $\mathbf{y}_{m_r}[n]$ into the corresponding frequency-domain (FD) received chip block sequence

$$\tilde{\mathbf{y}}_{m_r}[n] := \mathbf{F}_K \cdot \mathbf{y}_{m_r}[n],$$

with \mathbf{F}_K the $K \times K$ FFT matrix, leads to the following FD input/output relationship :

$$\tilde{\mathbf{y}}_{m_r}[n] = \tilde{\mathbf{H}}_{m_r} \cdot \tilde{\mathbf{x}}[n] + \tilde{\mathbf{z}}_{m_r}[n] \quad (83)$$

where

$$\tilde{\mathbf{z}}_{m_r}[n] := \mathbf{F}_K \cdot \mathbf{z}_{m_r}[n]$$

is the FD received noise block sequence,

$$\tilde{\mathbf{x}}[n] := \mathbf{F}_K \cdot (\mathbf{T}_1 \cdot \mathbf{x}[n] + \mathbf{T}_2 \cdot \mathbf{b})$$

is the FD transmitted chip block sequence and

$$\tilde{\mathbf{H}}_{m_r} := \text{diag}\{\tilde{\mathbf{h}}_{m_r}\}$$

is the $K \times K$ diagonal FD channel matrix having the FD channel response $\tilde{\mathbf{h}}_{m_r}$ for the m_r -th receive antenna as its main diagonal. Stacking the FD received chip block sequences of the M_r receive antennas, we finally obtain the following FD data model :

$$\underbrace{\begin{bmatrix} \tilde{\mathbf{y}}_1[n] \\ \vdots \\ \tilde{\mathbf{y}}_{M_r}[n] \end{bmatrix}}_{\tilde{\mathbf{y}}[n]} = \underbrace{\begin{bmatrix} \tilde{\mathbf{H}}_1 \\ \vdots \\ \tilde{\mathbf{H}}_{M_r} \end{bmatrix}}_{\tilde{\mathbf{H}}} \cdot \tilde{\mathbf{x}}[n] + \underbrace{\begin{bmatrix} \tilde{\mathbf{z}}_1[n] \\ \vdots \\ \tilde{\mathbf{z}}_{M_r}[n] \end{bmatrix}}_{\tilde{\mathbf{z}}[n]} \quad (84)$$

D.1.c Data model for burst processing

[0124] Assuming a burst length of $I \cdot B$ data symbols for each user, we can stack $I \cdot N$ consecutive FD received chip blocks $\tilde{y}[n]$, defined in Equation 84, into a FD output matrix

$$\tilde{\mathbf{Y}} := [\tilde{y}[0], \dots, \tilde{y}[IN-1]] ,$$

leading to the following FD data model for burst processing :

$$\tilde{\mathbf{Y}} = \tilde{\mathbf{H}} \cdot \tilde{\mathbf{X}} + \tilde{\mathbf{Z}} \quad (85)$$

where the FD input matrix $\tilde{\mathbf{X}}$ and the FD noise matrix $\tilde{\mathbf{Z}}$ are similarly defined as the FD output matrix $\tilde{\mathbf{Y}}$. Note from Equation 83 that :

$$\tilde{\mathbf{X}} = \mathbf{F}_K \cdot (\mathbf{T}_1 \cdot \mathbf{X} + \mathbf{T}_2 \cdot \mathbf{B}) \quad (86)$$

where the TD input matrix \mathbf{X} stacks $I \cdot N$ consecutive multi-user chip blocks $\mathbf{x}[n]$ and the KSP matrix \mathbf{B} repeats $I \cdot N$ times the known symbol postfix \mathbf{b} . By inspecting Equation 80, we can also write \mathbf{X} as follows :

$$\mathbf{X} = \mathbf{S}_d \cdot \mathbf{C}_d + \mathbf{S}_p \cdot \mathbf{C}_p \quad (87)$$

where the multi-user data symbol matrix

$$\mathbf{S}_d := [\mathbf{S}_1, \dots, \mathbf{S}_U]$$

stacks the data symbol matrices of the different active users and the u -th user's data symbol matrix

$$\mathbf{S}_u := [s^u[0], \dots, s^u[I-1]]$$

stacks I consecutive data symbol blocks of the u -th user. The pilot symbol matrix \mathbf{S}_p is similarly defined as \mathbf{S}_d . The multi-user code matrix

$$\mathbf{C}_d := [\mathbf{C}_1^T, \dots, \mathbf{C}_U^T]^T$$

stacks the code matrices of the different active users. The u -th user's code matrix

$$\mathbf{C}_u := \text{diag} \{ \mathbf{c}_u[0], \dots, \mathbf{c}_u[I-1] \}$$

stacks the u -th user's composite code vectors at I consecutive symbol instants, and the u -th user's composite code vector

$$\mathbf{c}_u[i] := [c_u[iN], \dots, c_u[(i+1)N-1]]$$

is used to spread the data symbol block $\mathbf{s}^u[l]$. The pilot code matrix \mathbf{C}_p and the pilot composite code vector $\mathbf{c}_p[l]$ are similarly defined as \mathbf{C}_d respectively $\mathbf{c}_d[l]$.

D.2 Burst frequency-domain chip equalization

[0125] Armed with a suitable data model for burst processing, we can now proceed with the design of different Least Squares (LS) type of burst frequency-domain (FD) chip equalizers that process a burst of L data symbol blocks at once. Note that Recursive Least Squares (RLS) type of adaptive FD chip equalizers that process the data on a symbol block by symbol block basis can be easily derived from their corresponding LS burst version. Starting from Equation 85 and assuming the total FD channel matrix $\tilde{\mathbf{h}}$ to have full column rank and the FD input matrix $\tilde{\mathbf{X}}$ to have full row rank, it is always possible to find a Zero-Forcing (ZF) FD chip equalizer matrix $\tilde{\mathbf{G}}$, for which

$$\tilde{\mathbf{G}} \cdot \tilde{\mathbf{Y}} - \tilde{\mathbf{X}} = 0,$$

provided there is no noise present in the FD output matrix $\tilde{\mathbf{Y}}$. In the presence of noise, we have to solve the corresponding Least Squares (LS) minimization problem, which we denote for convenience as:

$$\tilde{\mathbf{G}} \cdot \tilde{\mathbf{Y}} - \tilde{\mathbf{X}} \stackrel{LS}{=} 0 \quad (88)$$

[0126] Since the total FD channel matrix $\tilde{\mathbf{h}}$ stacks M_r diagonal FD channel matrices, as indicated by Equations 83 and 84, the total FD equalizer matrix $\tilde{\mathbf{G}}$ suffices to have a similar structure :

$$\tilde{\mathbf{G}} := [\tilde{\mathbf{G}}_1 \quad \dots \quad \tilde{\mathbf{G}}_{M_r}] \quad (89)$$

where the FD equalizer matrix for the m_r -th receive antenna

$$\tilde{\mathbf{G}}_{m_r} := \text{diag} \{ \tilde{\mathbf{g}}_{m_r} \}$$

acts on a per-tone basis at the chip block level (see also Figure 10). Using Equations 86 and 87, we can rewrite the original LS problem of Equation 88 as :

$$\tilde{\mathbf{G}} \cdot \tilde{\mathbf{Y}} - \tilde{\mathbf{S}}_d \cdot \mathbf{C}_d - \tilde{\mathbf{S}}_p \cdot \mathbf{C}_p - \mathbf{F}_K \cdot \mathbf{T}_2 \cdot \mathbf{B} \stackrel{LS}{=} 0 \quad (90)$$

where the FD multi-user data symbol matrix $\tilde{\mathbf{S}}_d$ and the FD pilot symbol matrix $\tilde{\mathbf{S}}_p$ are defined as :

$$\tilde{\mathbf{S}}_d := \mathbf{F}_K \cdot \mathbf{T}_1 \cdot \mathbf{S}_d \tilde{\mathbf{S}}_p := \mathbf{F}_K \cdot \mathbf{T}_1 \cdot \mathbf{S}_p \quad (91)$$

[0127] Starting from Equation 90, we will design in the following three different FD methods for direct chip equalizer estimation that differ in the amount of a-priori information they exploit to determine the equalizer coefficients. The first method, coined KSP-trained, only exploits the presence of a known symbol postfix. The last two methods, coined joint CDMP/KSP-trained and semi-blind joint CDMP/KSP-trained, exploit the presence of both a known symbol postfix and a Code Division Multiplexed Pilot (CDMP).

D.2.a KSP-trained chip equalizer

[0128] The KSP-trained chip equalizer estimator directly determines the equalizer coefficients from the FD output matrix $\tilde{\mathbf{Y}}$ based on the knowledge of the KSP matrix \mathbf{B} . By transforming Equation 90 to the TD with the IFFT matrix \mathbf{F}_K^H

and by selecting the known symbol postfix with the KSP transmit matrix T_2 , we obtain :

$$T_2^T \cdot F_K^H \cdot \tilde{G} \cdot \tilde{Y} - B = 0 \quad (92)$$

because $T_2^T \cdot T_1 = 0_{\mu \times B}$ and $T_2^T \cdot T_2 = I_\mu$. Using the definition of T_2 in Equation 81, we can rewrite Equation 92 as :

$$F_K^H(B+1:K,:) \cdot \tilde{G} \cdot \tilde{Y} - B = 0 \quad (93)$$

which can be interpreted as follows. The equalized FD output matrix

$$\tilde{G} \cdot \tilde{Y}$$

is transformed to the TD with the last μ rows of the IFFT matrix F_K^H . The resulting matrix should be as close as possible to the KSP matrix B in a Least Squares sense.

D.2.B Joint CDMP/KSP-trained chip equalizer

[0129] The joint CDMP/KSP-trained chip equalizer estimator directly determines the equalizer coefficients from the FD output matrix \tilde{Y} based on the knowledge of the pilot code matrix C_p , the pilot symbol matrix S_p and the KSP matrix B . By despreading Equation 90 with the pilot code matrix C_p , we obtain :

$$\tilde{G} \cdot \tilde{Y} \cdot C_p^H - F_K \cdot (T_1 \cdot S_p + T_2 \cdot B \cdot C_p^H) = 0 \quad (94)$$

because $C_d \cdot C_p^H = 0$ due to the orthogonality of the user and the pilot composite code sequences at each symbol block instant. Equation 94 can be interpreted as follows. The equalized FD output matrix after despreading

$$\tilde{G} \cdot \tilde{Y} \cdot C_p^H$$

should be as close as possible in a Least Squares sense to the FD version of the pilot symbol matrix S_p padded with the KSP matrix after despreading $B \cdot C_p^H$.

D.2.c Semi-blind joint CDMP/KSP-trained chip equalizer

[0130] The semi-blind joint CDMP/KSP-trained chip equalizer estimator directly determines the equalizer coefficients from the FD output matrix \tilde{Y} based on the knowledge of the multi-user code matrix C_d , the pilot code matrix C_p , the pilot symbol matrix S_p and the KSP matrix B . By despreading Equation 90 with the multi-user code matrix C_d and by assuming the FD equalizer matrix \tilde{G} to be known and fixed, we obtain an LS estimate of the multi-user data symbol matrix \hat{S}_d :

$$\hat{S}_d = \tilde{G} \cdot \tilde{Y} \cdot C_d^H - F_K \cdot T_2 \cdot B \cdot C_d^H \quad (95)$$

because $C_p \cdot C_d^H = 0_{1 \times U}$ due to the orthogonality of the pilot and user composite code sequences at each symbol block instant. Substituting \hat{S}_d into the original LS problem of Equation 90 leads to :

$$\tilde{G} \cdot \tilde{Y} \cdot P_d - F_K \cdot (T_1 \cdot S_p \cdot C_p + T_2 \cdot B \cdot P_d) = 0 \quad (96)$$

where the projection matrix \mathbf{P}_d is defined as :

$$\mathbf{P}_d = \mathbf{I}_N - \mathbf{C}_d^H \mathbf{C}_d \quad (97)$$

[0131] Equation 96 can be interpreted as follows. Both the FD output matrix $\tilde{\mathbf{Y}}$ and the KSP matrix \mathbf{B} are projected on the orthogonal complement of the subspace spanned by the multi-user code matrix \mathbf{C}_d , employing the projection matrix \mathbf{P}_d . The equalized FD output matrix after projecting

$$\tilde{\mathbf{G}} \cdot \tilde{\mathbf{Y}} \cdot \mathbf{P}_d$$

should then be as close as possible in a Least Squares sense to the FD version of the pilot chip matrix $\mathbf{S}_p \cdot \mathbf{C}_p$ padded with the KSP matrix after projection $\mathbf{B} \cdot \mathbf{P}_d$.

D.2.d User-specific detection

[0132] As shown in Figure 10, the obtained FD chip equalizer matrix $\hat{\mathbf{c}}$, whether KSP-trained, joint CDMP/KSP-trained or semi-blind joint CDMP/KSP-trained, may subsequently be used to extract the desired user's data symbol matrix :

$$\hat{\mathbf{S}}_u = \mathbf{T}_1^T \cdot \underbrace{\mathbf{F}_K^H \cdot \hat{\mathbf{G}} \cdot \tilde{\mathbf{Y}} \cdot \mathbf{C}_u^H}_{\hat{\mathbf{X}}} \quad (98)$$

where the estimated FD input matrix $\hat{\mathbf{X}}$ is transformed to the TD by the IFFT matrix \mathbf{F}_K^H and has its known symbol postfix removed by the ZP transmit matrix \mathbf{T}_1 . The resulting estimate of the TD input matrix \mathbf{X} is finally despread with the desired user's code matrix \mathbf{C}_u to obtain an estimate of the desired user's data symbol matrix.

D.3 Conclusion

[0133] In this section, three new direct equalizer estimation methods have been developed for single-carrier block transmission (SCBT) DS-CDMA with Known Symbol Padding (KSP). The KSP-trained method, that only exploits the presence of the known symbol postfix, only achieves reasonable performance for rather large burst lengths. The joint CDMP/KSP-trained method, that additionally exploits the presence of a Code Division Multiplexed Pilot (CDMP), outperforms the KSP-trained method for both large and small burst lengths. The semi-blind joint CDMP/KSP-trained method, that additionally capitalizes on the unused spreading codes in a practical CDMA system (assuming knowledge of the multi-user code correlation matrix) proves its usefulness especially for small burst lengths. It outperforms the regular joint CDMP/KSP-trained method while staying within reasonable range of the ideal fully-trained method.

[0134] One can conclude that, from a performance point of view, the semi-blind joint CDMP/KSP-trained method for direct equalizer estimation is an interesting technique for future broadband cellular systems based on SCBT-DS-CDMA.

Claims

1. In a communication system having at least one base station and at least one terminal, a method for multi-user wireless transmission of data signals, comprising, for a plurality of users, the following steps :

- adding robustness to frequency-selective fading to said data to be transmitted,
- performing spreading and scrambling of at least a portion of a block of data, obtainable by grouping data symbols by demultiplexing using a serial-to-parallel operation,
- combining (summing) spread and scrambled portions of said blocks of at least two users,
- adding transmit redundancy to said combined spread and scrambled portions, and
- transmitting said combined spread and scrambled portions with transmit redundancy.

2. The method recited in 1, wherein said spreading and scrambling is performed by a code sequence, obtained by multiplying a user (terminal)-specific code and a base station specific scrambling code.
- 5 3. The method recited in 1, wherein preceding the steps of claim 1, the step is performed of generating a plurality of independent block portions.
4. The method recited in 1, wherein preceding the steps of claim 1, the step is performed of generating block portions.
- 10 5. The method recited in 3, wherein all the steps are performed as many times as there are block portions, thereby generating streams comprising a plurality of combined spread and scrambled block portions.
6. The method recited in 5, wherein between the step of combining and the step of transmitting said spread and scrambled portions, the step is comprised of encoding each of said streams.
- 15 7. The method recited in 5, wherein between the step of combining and the step of transmitting said spread and scrambled portions, the step is comprised of space-time encoding said streams, thereby combining info from at least two of said streams.
- 20 8. The method recited in 7, wherein said step of space-time encoding said streams is performed by block space-time encoding or trellis space-time encoding.
9. The method recited in 1, wherein between the step of combining and the step of transmitting said spread and scrambled portions, the step of inverse subband processing is comprised.
- 25 10. The method recited in 1, wherein the step of adding robustness to frequency-selective fading is performed by adding linear precoding.
11. The method recited in 1, wherein the step of adding robustness to frequency-selective fading is performed by applying adaptive loading per user.
- 30 12. The method recited in 1, wherein the step of combining spread and scrambled block portions includes the summing of a pilot signal.
13. The method recited in 1, wherein the step of adding transmit redundancy comprises the addition of a cyclic prefix, a zero postfix or a symbol postfix.
- 35 14. A transmit system device for wireless multi-user communication, applying the method recited in 1.
- 40 15. A transmit apparatus for wireless multi-user communication, comprising :
 - Circuitry for grouping data symbols to be transmitted,
 - Means for applying a spreading and scrambling operation to said grouped data symbols,
 - Circuitry for add transmit redundancy to said spread and scrambled grouped data symbols, and
 - 45 - At least one transmit antenna for transmitting said spread and scrambled grouped data symbols with transmit redundancy.
16. The transmit apparatus recited in 15, further comprising means for adding robustness to frequency-selective fading to said grouped data symbols.
- 50 17. The transmit apparatus recited in 15, further comprising a space-time encoder.
18. The transmit apparatus recited in 15, further comprising one or more circuits adapted for inverse subband processing said grouped data symbols.
- 55 19. A method for receiving at least one signal in a multi-user wireless communication system having at least one base station and at least one terminal, comprising the steps of
 - Receiving a signal from at least one antenna,

- Subband processing of a version of said received signal,
- Separating the contributions of the various users in said received signal
- Exploiting the additional robustness to frequency-selective fading property of said received signal.

- 5 20. The method recited in 19, wherein the step of separating the contributions consists in first filtering at chip rate at least a portion of the subband processed version of said received signal and then despreading.
21. The method recited in 19, wherein the step of separating the contributions consists in first despreading and then filtering at least a portion of the subband processed version of said received signal.
- 10 22. The method recited in 19, wherein the step of receiving a signal is performed for a plurality of antennas, thereby generating data streams and wherein the step of subband processing is performed on each of said data streams, yielding a subband processed version of said received signal.
- 15 23. The method recited in 19, wherein the additional step of space-time decoding is performed on each of the streams.
24. The method recited in 23, wherein the step of space-time decoding is performed by block decoding or trellis decoding.
- 20 25. The method recited in 20 or 21, wherein the additional step of inverse subband processing is performed on at least one filtered, subband processed version of the received signal.
26. The method recited in 20 or 21, wherein the step of filtering is carried out by a filter of which the filter coefficients are determined in a semi-blind fashion or in a training-based way.
- 25 27. The method recited in 20 or 21, wherein the step of filtering is carried out by a filter of which the filter coefficients are determined without channel estimation.
28. The method recited in 20 or 21, wherein the step of filtering is carried out by a filter of which the filter coefficients are determined such that one version of the filtered signal is as close as possible to a version of the pilot symbol.
- 30 29. The method recited in 28, wherein the version of the filtered signal is the filtered signal after despreading with a composite code of the base station specific scrambling code and the pilot code and wherein the version of the pilot symbol is the pilot symbol itself, put in per tone ordering.
- 35 30. The method recited in 28, wherein the version of the filtered signal is the filtered signal after projecting on the orthogonal complement on the subspace spanned by the composite codes of the base station specific scrambling code and the user specific codes, and wherein the version of the pilot symbol is the pilot symbol spread with a composite code of the base station specific scrambling code and the pilot code, and put in per tone ordering.
- 40 31. The method recited in 19, wherein the additional step of removing transmit redundancy is performed.
32. The method recited in 19, wherein said additional robustness to fading is exploited by linear de-precoding.
- 45 33. A receive system device for wireless multi-user communication, applying the method recited in 19.
34. A receiver apparatus for wireless multi-user communication, comprising :
 - A plurality of antennas receiving signals,
 - 50 - A plurality of circuits adapted for subband processing of said received signals,
 - Circuitry being adapted for determining by despreading an estimate of subband processed symbols received by at least one user.
35. The apparatus recited in 34, wherein said circuitry adapted for determining an estimate of symbols comprises a plurality of circuits for inverse subband processing.
- 55 36. The apparatus recited in 34, wherein said circuitry adapted for determining an estimate of symbols further comprises a plurality of filters to filter at least a portion of a subband processed version of said received signals.

37. The apparatus recited in 34, wherein said circuitry adapted for determining an estimate of symbols further comprises a plurality of filters to filter at chip rate at least a portion of a subband processed version of said received signals.

5 38. The apparatus recited in 34, further comprising a space-time decoder.

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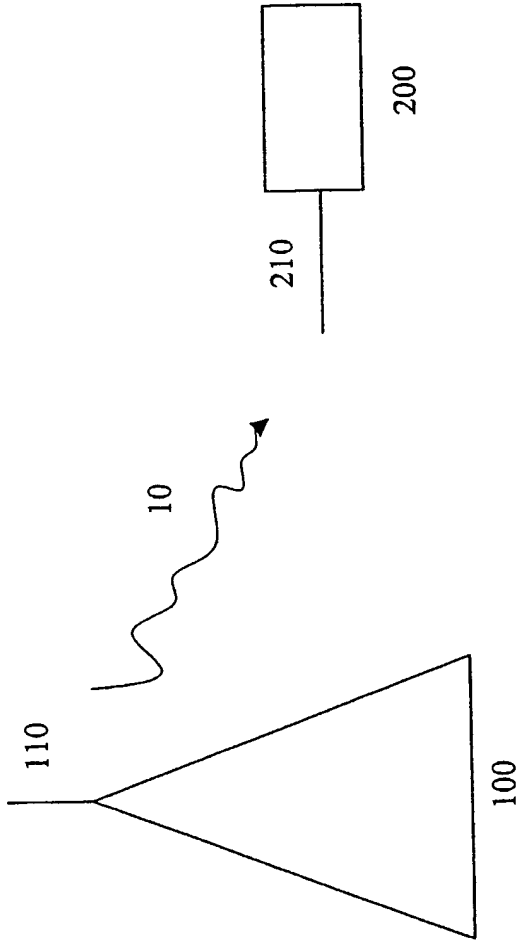


Fig. 1

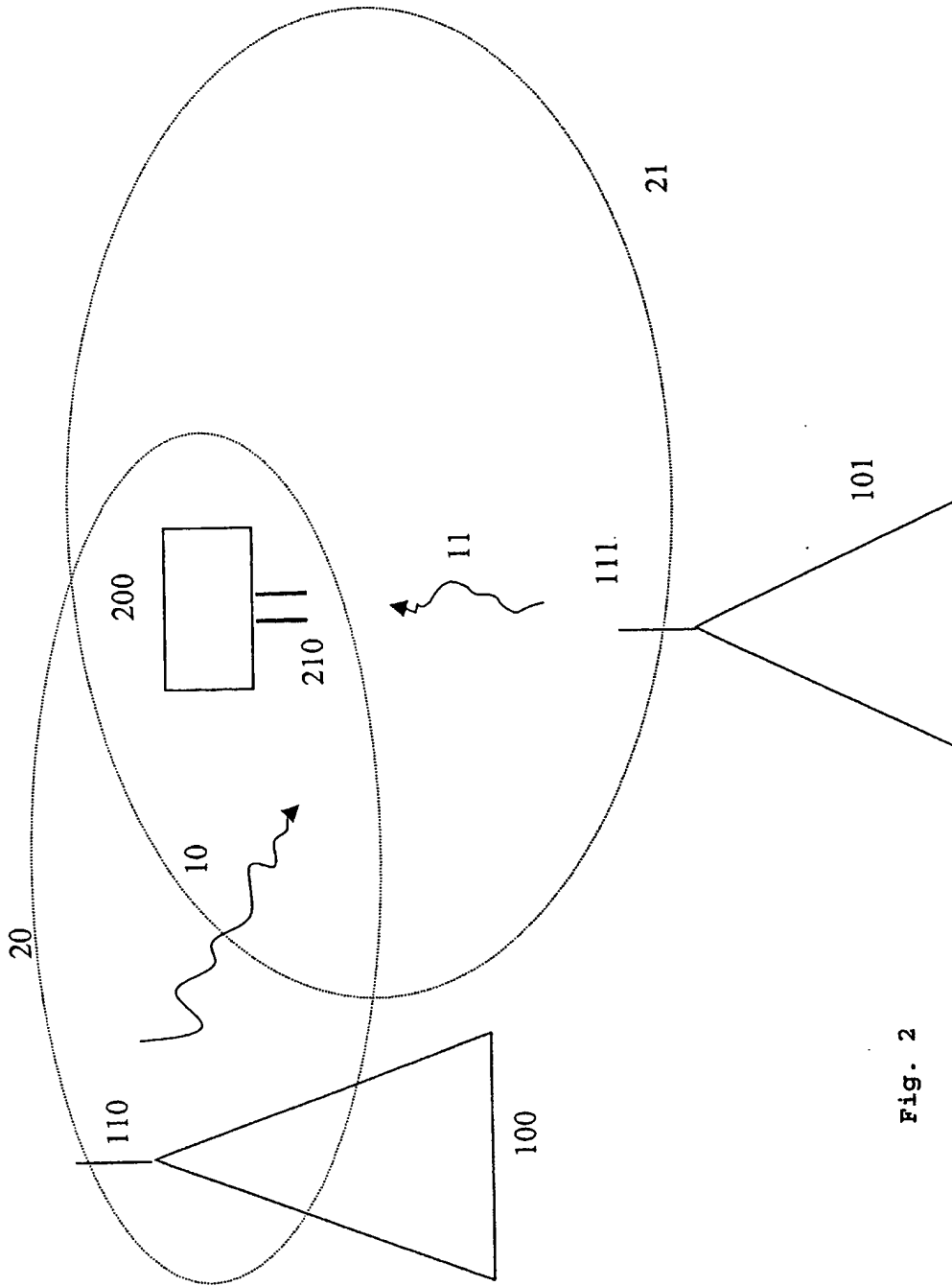


Fig. 2

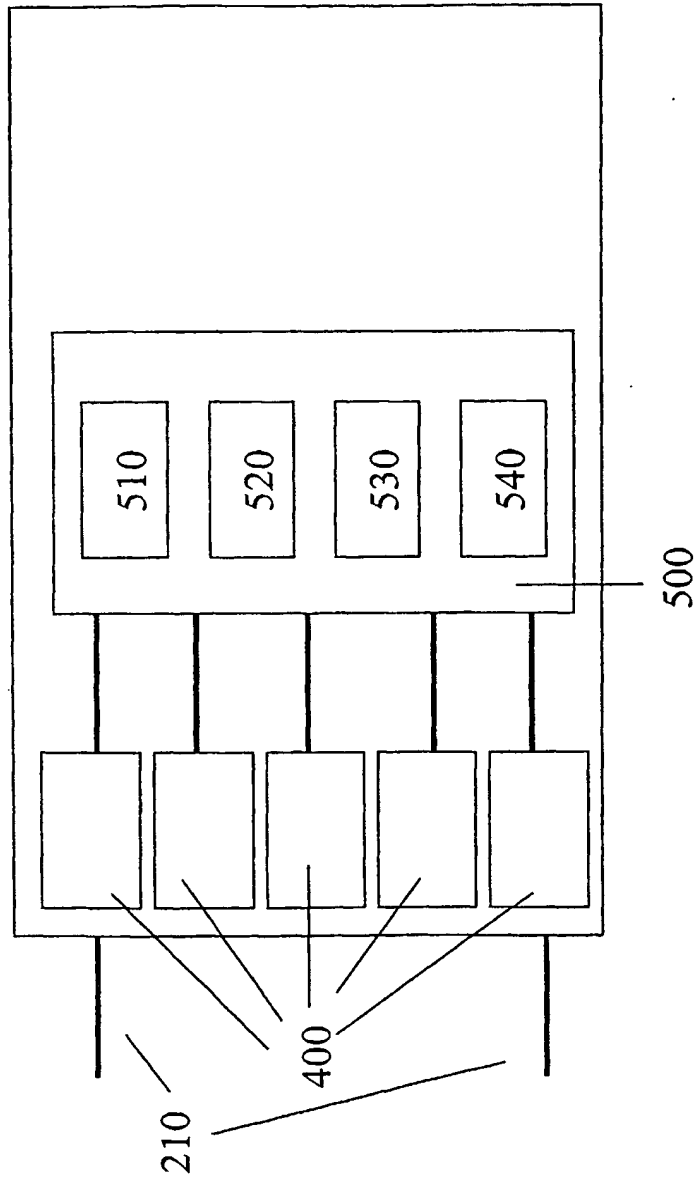


Fig. 3

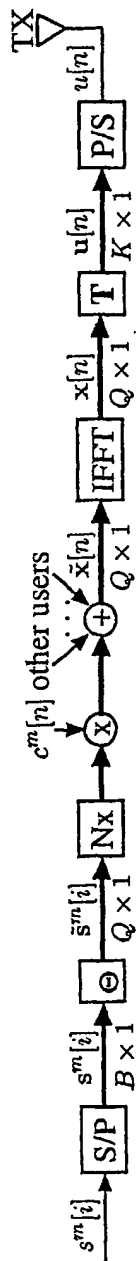


Fig. 4

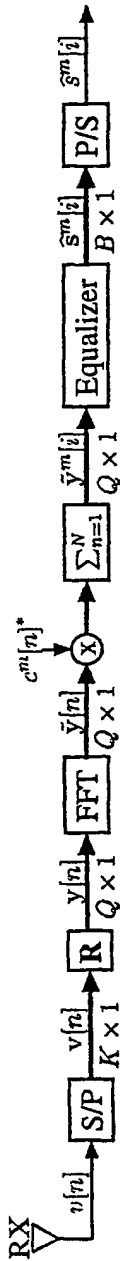


Fig. 5

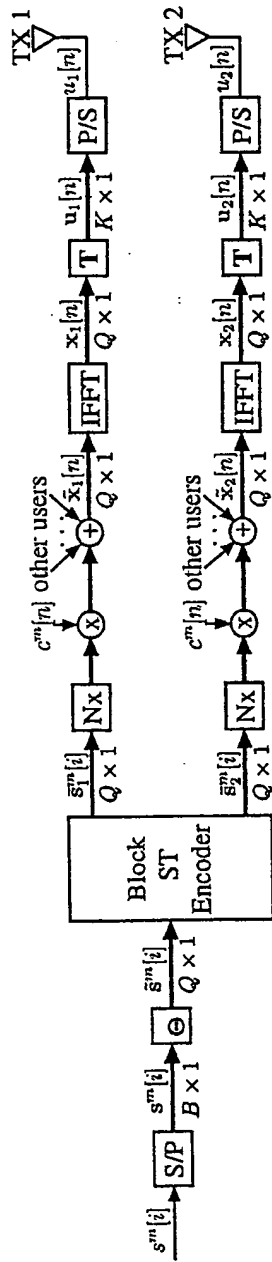


Fig. 6

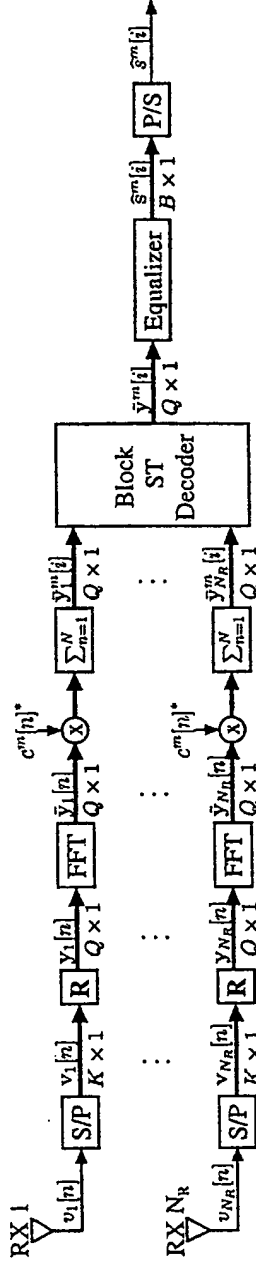


Fig. 7

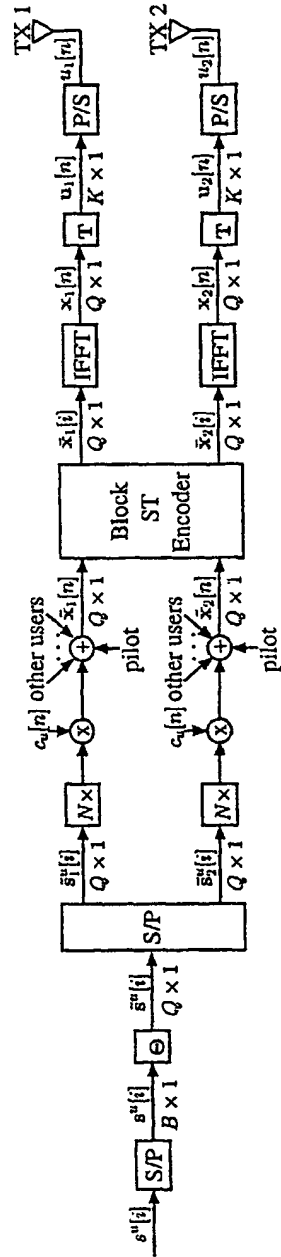


Fig. 8

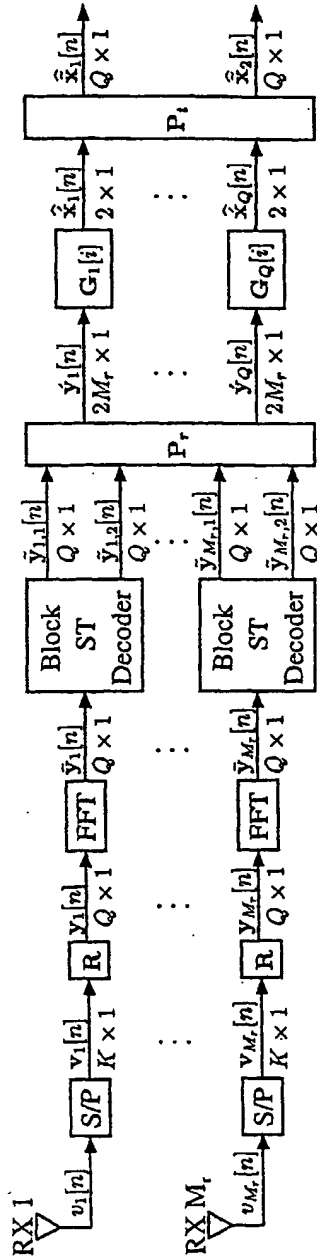


Fig. 9

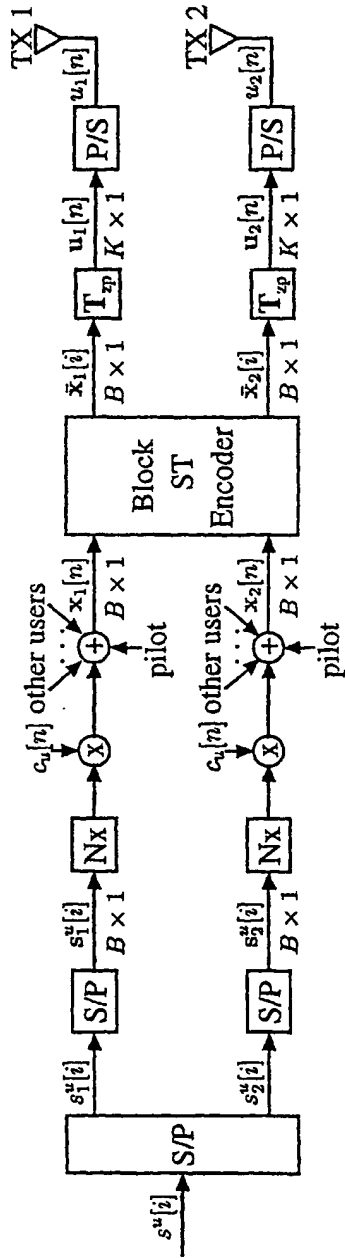


Fig. 10

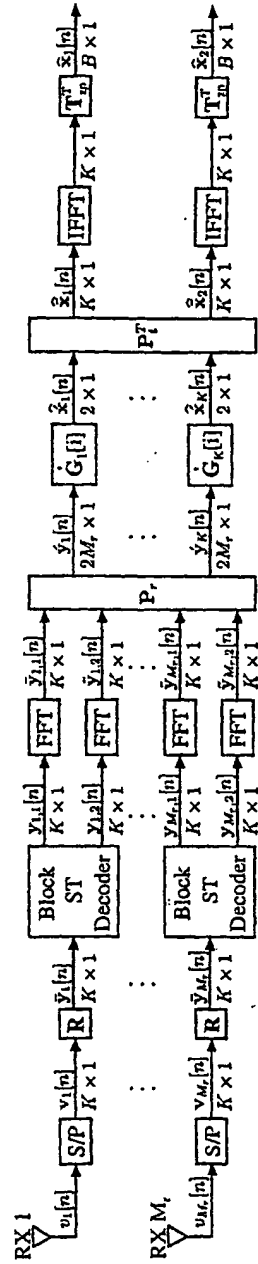
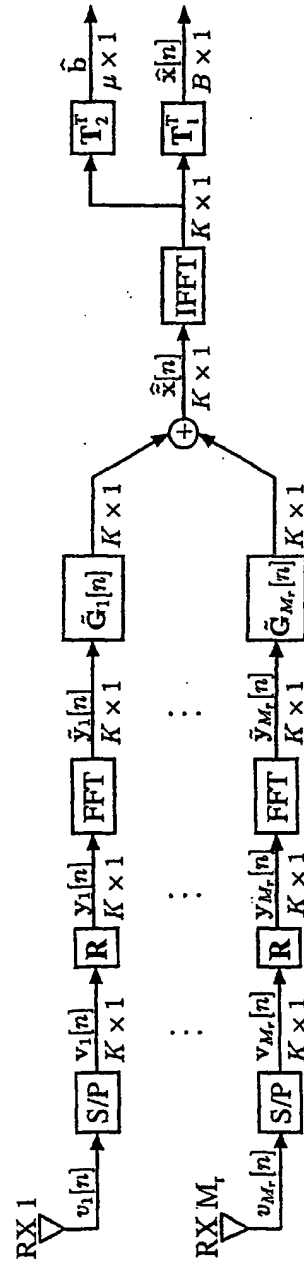
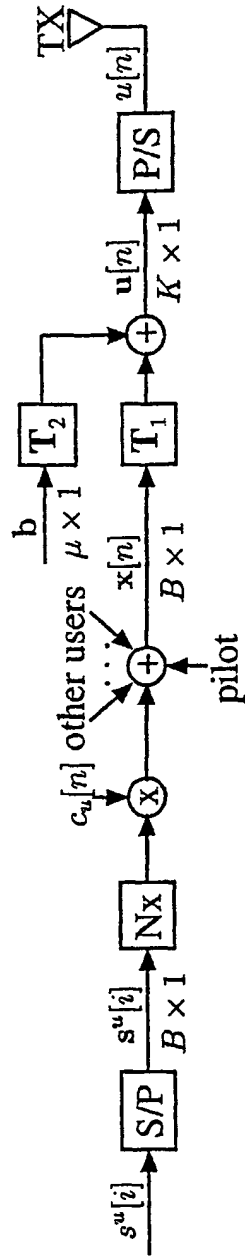


Fig. 11





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Application Number
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